14th International Workshop on Worst-Case Execution Time Analysis

WCET 2014, July 18, 2014, Madrid, Spain

Edited by

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Welcome to WCET 2014

It is my great pleasure to welcome you to the *14th International Workshop on Worst-Case Execution Time Analysis (WCET 2014)*. This year's edition of WCET continues its tradition of being the premier forum for presentation of research results in the fields of hard real-time systems, predictability and timing analysis. One important goal of WCET is to provide a link between the timing analysis and the formal analysis, computer architecture and compiler communities. Researchers and developers in these areas are addressing many similar problems, but with different backgrounds and approaches. WCET is intended to expose researchers and developers from either area to relevant work and interesting problems in the other area and to provide a forum where they can interact.

The call for papers attracted 17 submissions from Asia, Europe and North America. The program committee accepted 10 papers that cover a variety of topics, including formal methods for WCET and value analysis, multicore challenges, timing analyses for emerging cache architectures and software approaches supporting WCET analysis. In addition, the program includes a keynote talk given by Vincent Nélis on the challenge of time-predictability in modern many-core architectures, and a report by Christine Rochange on the 2014 edition of the WCET Tool Challenge.

Putting together WCET 2014 was a team effort. First of all, I would like to thank the authors and Vincent and Christine for providing the content of the program. I would like to express my gratitude to the program committee and reviewers who worked very hard in reviewing papers and providing suggestions for their improvements.

WCET 2014 is being organized as satellite workshop of the 26th Euromicro Conference on Real-Time Systems (ECRTS 2014). I am therefore grateful to the ECRTS 2014 general chair, Juan Antonio de la Puente, his local team, and the Real-Time Technical Committee Chair of Euromicro, Gerhard Fohler, for their support. This year's WCET workshop were not possible without external support – the financial support by the EU COST Action IC1202: Timing Analysis on Code-Level (TACLe) and by the COST Office is highly appreciated.

I hope that you will find this program interesting and thought-provoking and that the workshop will provide you with a valuable opportunity to share ideas with other researchers and practitioners. These proceedings will hopefully serve as worthwhile reference for researchers in the timing analysis and real-time systems domains, enjoy reading this volume.

Heiko Falk

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