15th International Workshop on Worst-Case Execution Time Analysis

WCET 2015, July 7, 2015, Lund, Sweden

Edited by

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To my family in the Canary Islands and Barcelona.
Specially to David, Alex, Paula and Marcos (a natural-born fighter!).

Welcome to WCET 2015

It has been my pleasure to serve as the PC Chair of the WCET Analysis Workshop 2015 (WCET 2015). I want to start by expressing my gratitude to the authors, the PC members and the external reviewers for their effort. You have made possible to have an excellent program for WCET 2015!

WCET 2015 continues to be the reference forum for academics, practitioners and industrialists in the field of timing analysis for hard real-time systems. In this edition the WCET Analysis workshop has tried to broaden its scope to cover any aspect related to the timing analysis of computer systems. This is motivated by the fact that while in the past timing analysis has been a topic mainly for real-time systems, recently it has becoming crucial in other domains dealing with timing guarantees. This includes among other mobile computing and high-performance computing. Hence, this edition of the WCET workshop, besides papers targeting traditional WCET analysis, encouraged submissions focused on less rigorous and mature timing analysis techniques on complex multicore and manycore heterogeneous, usually COTS, architectures. For such complex architectures Execution Time Bound (ETB) estimates are derived rather than WCET estimates in the strict sense. ETB estimates are intrinsically less reliable than WCET estimates.

We received 26 good-quality submissions out of which 10 were selected for presentation and the proceedings. This has resulted in a solid program for WCET 2015 which is also strengthened with an excellent keynote by Jon Perez. Jon is the Embedded Systems research line coordinator at IK4-IKERLAN. His talk will cover aspects related to multicore, WCET and certification in the context of fail-safe mixed-criticality systems.

WCET 2015 has been also possible thanks to the financial support of the EU COST Action IC1202 on Timing Analysis on Code-Level (TACLe); the FP7 IP Project PROXIMA (`http://www.proxima-project.eu/`) and the FP7 STREP Project P-SOCRATES (`http://www.p-socrates.eu/`). Special thanks also to our supporting institution the HiPEAC Network of Excellence (`http://www.hipeac.net`).

I welcome all participants to the WCET 2015 and I encourage them to be active and enjoy the opportunity for discussion and interaction with other workshop attendees.

See you in Lund!
Francisco J. Cazorla

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