

# Synchronous Programming—SYNCHRON '04

## Dagstuhl Seminar

### Executive Summary\*

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This seminar was the 11th in a series of semi-annual workshops on the Synchronous Languages (Esterel, Lustre, and Signal). These languages were invented in the early 1980's to make the programming of reactive systems easier. Benveniste et al. [3] presents a recent survey of the field.

## 1 The Seminar

The goal of the seminar was to bring together researchers and practitioners of synchronous programming, and furthermore to reach out to relevant related areas and industrial users. With a record participation in this year's SYNCHRON workshop and a broad range of topics discussed, the aims seem to have been well-met. The program of the seminar was composed of around thirty presentations, all of which included extensive technical discussions. The fields covered included synchronous semantics, modeling languages, verification, heterogeneous and distributed systems, hardware/software integration, reactive processing, timing analyses, application experience reports, and industrial requirements.

Particularly successful this year were presentations from the automotive industry. Stefan-Alexander Schneider and Thomas Stauner both discussed issues with real-time software development at BMW. Matthias Hoffmann represented DaimlerChrysler.

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\*Alain Girault wrote the first draft of this summary.

## 2 Reactive Systems

Any automatic control software is classified as a reactive system [9]. Indeed, such software must react continuously to their environment. They differ from interactive systems (e.g., operating systems) because their reaction speed is imposed by the environment—because the environment cannot wait. Examples of such software include nuclear power plant controllers, aircraft flight systems, automotive controls, and so forth.

The essential characteristics of reactive systems are

- **Criticality:** They are highly critical (e.g., time-critical), just like the systems they control are critical.
- **Parallelism:** At least the parallelism between the system and its environment must be taken into account during the specification. Moreover, it is often convenient for the designer to conceive of the system as a set of parallel components cooperating in order to achieve the desired behaviour.
- **Determinism:** A deterministic system determines a sequence of output signals from a sequence of input signals in a unique way. This property makes its design, analysis, and debugging much easier. For critical systems, this choice is obvious.

## 3 The Classical Approach

Classical programming tools are not well-suited to reactive systems programming. Automata-based systems lack high-level parallel programming primitives while asynchronous languages do not respect the intrinsic determinism of reactive systems. Asynchronous language inherit from the field of operating systems and time sharing. In particular, asynchronous parallelism amounts to interleaving. The construct  $a \parallel b$  (run  $a$  in parallel with  $b$ ) is either implemented as  $a;b$  (execute  $a$  then  $b$ ) or as  $b;a$ , thus introducing an unwanted non-determinism. This is the case of well-known languages like Occam and Ada, which use the rendezvous-based mechanism inspired by CSP. It is also the case of SDL which uses waiting queues inspired by Petri Nets.

## 4 Synchronous Abstraction

Synchronous languages are instead based on the simultaneity principle: The construct  $a \parallel b$  is implemented as the unit  $ab$ , leaving to the compiler any choice of detailed scheduling. Another way of viewing a synchronous program consists of saying that all the parallel processes evolve simultaneously, sharing a common discrete time scale. This is known as the logical time abstraction: all processes compute one discrete time step simultaneously. This is the approach taken by the synchronous language Esterel [5].

Another approach is to view a synchronous program as a dynamic system, specified as a system of dynamic equations. The job of the synchronous compiler consists, then, of solving this system of equations. This is the approach taken by the synchronous languages Lustre [6, 7] and Signal [10].

## 5 Advantages of the Synchronous Approach

There are numerous advantages to the synchronous approach. The main one is that the temporal semantics is simplified, thanks to the aforementioned logical time abstraction. This leads to clear temporal constructs and easier time reasoning. Just like ML and Pascal are high-level sequential programming languages in the sense that they are typed and structured, synchronous languages are high-level parallel languages in the sense that they are temporally typed and structured. Programming with ML reduces functional bugs; programming with synchronous languages reduces temporal bugs.

Another key advantage is the reduction of the state-space explosion problem from the discrete logical time abstraction. Synchronous systems evolves in a sequence of discrete steps, and nothing occurs between two successive steps (this is as opposed to models with interleaving concurrent semantics, such as concurrent Java). This makes program debugging, testing, and validating easier. In particular, formal verification of synchronous programs is possible with techniques like model checking. Another consequence is that synchronous language compilers are able to generate automatically embeddable code with performances that can be measured precisely. Hence the reaction time of the software can be known at compile time and can be compared with the desired sampling period. Control engineers can specify and tune their automatic control algorithm with synchronous languages and then rely on the compiler to generate automatically embeddable code, therefore avoiding the tedious and error-prone task of actually implementing the code corresponding to their algorithm.

## 6 Synchronous Languages

Historically, the first synchronous language is Esterel [4, 5], developed at the Centre de Mathématiques Appliquées (CMA) of École des Mines de Paris, in Sophia-Antipolis, France, and later joined by people from INRIA. It is an imperative language that was originally inspired by CCS and SCCS. Esterel introduces constructs like preemption and communication by synchronous broadcast. It is devoted to the programming of discrete event systems. Esterel Technologies now markets an industrial version of the Esterel compiler. There exists several other synchronous languages. This is just a selection, presented in chronological order:

- Lustre [6] is a data-flow declarative functional language also inspired by Lucid. The Scade tool, initially developed by Verilog and Aerospatiale is based on Lustre. Scade is now marketed by Esterel Technologies.

- Signal [10] is also a data-flow declarative language, but it is relational instead of functional like Lustre. In this sense, it is more general than Lustre. Polychrony is the public domain Signal compiler, while Sildex is the commercial tool developed by TNI-Valiosys.
- Argos [11] is a purely synchronous version of the well known Statecharts formalism [8], which yields a number of advantages. In particular, Argos has a compositional semantics. SyncCharts [1] and Mode Automata are both inspired from Argos.
- Polis [2] is a graphical tool for implementing Codesign Finite State Machines (CFSM). The model of computation behind CFSMs is a set of synchronous FSMs communicating asynchronously; It is therefore known as Globally Asynchronous Locally Synchronous (GALS). The Ciertto VCC tool developed by Cadence is based on Polis.
- SL, the Synchronous Language, is a variant of Esterel where hypotheses about signal presence or absence are not allowed. Whether a given signal is present or absent can only be decided at the end of a synchronous instant, hence reaction to a signal is delayed until the next instant. The main advantage is that causality problems are avoided. SL was the starting point of many other synchronous languages such as Sugar Cubes and Junior.

While Esterel, Argos, and SL are more suited to discrete event systems, Lustre, Signal and Polis are very close to the specification formalisms used by automatic control engineers: block diagrams, differential equations, data flow networks, automata, and so on.

## 7 Industrial Impact

Synchronous languages have recently seen a tremendous interest from leading companies developing automatic control software for critical applications, such as Schneider, Dassault, Aerospatiale, Snecma, Cadence, Texas, and Thomson. For instance, Lustre is used to develop the control software for nuclear plants and Airbus planes. Esterel is used to develop DSP chips for mobile phones, to design and verify DVD chips, and to program the flight control software of Rafale fighters. And Signal is used to develop digital controllers for airplane engines. The key advantage pointed by these companies is that the synchronous approach has a rigorous mathematical semantics which allows the programmers to develop critical software faster and better.

## 8 Summary

In summary, synchronous programming is an interesting approach for designing and programming automatic control software. Synchronous languages have

a well-founded mathematical semantics that allow ideal temporal constructs as well as formal verification of the programs and automatic code generation. We believe they are ideally suited to programming automatic control software because they are close to the classic specification formalisms used by control engineers, and also because they offer code generation tools that avoid the tedious and error-prone task of implementing the control algorithm after having specified it. These nice features have been confirmed by their recent successes in the automatic control industry.

## Participants

This year's participation in the SYNCHRON workshop was the highest ever, which we attribute largely to the excellent Dagstuhl facilities.

Joaquin Aguado , Universität Bamberg  
Albert Benveniste, IRISA/INRIA Rennes  
Gérard Berry, Esterel Technologies - Villeneuve  
Reinhard Budde, Fraunhofer Inst. - St. Augustin  
Zbigniew Chamski, Philips Research - Eindhoven  
Jean-Louis Colaco, Esterel Technologies - Toulouse  
Gwenáél Delaval, INRIA Rhône-Alpes  
Stephen A. Edwards, Columbia University  
Harald Fecher, Universität Kiel  
Abdoulaye Gamatié, Université de Rennes  
Peter Gammie, Chalmers UT - Göteborg  
Alain Girault, INRIA Rhône-Alpes  
Gregor Goessler, INRIA Rhône-Alpes  
Claude Helmstetter, VERIMAG - IMPG  
Matthias Hoffmann, DaimlerChrysler Research - Berlin  
Leszek Holenderski, Philips Research - Eindhoven  
Ralf Huuck, National ICT Australia - Eveleigh  
Erwan Jahier, VERIMAG - IMPG  
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Xin Li, Universität Kiel  
Jan Lukoschus, Universität Kiel  
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Gordon Pace, University of Malta  
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Norman R. Scaife, VERIMAG - IMPG  
Klaus Schneider, TU Kaiserslautern  
Stefan-Alexander Schneider, BMW AG - München  
Satnam Singh, Microsoft - Seattle  
Cristian Soviani, Columbia University  
Thomas Stauner, BMW Car IT  
Walid Taha, Rice University  
Jean-Pierre Talpin, INRIA Rennes  
Olivier Tardieu, INRIA - Sophia Antipolis  
Stephan Thesing, Universität Saarbrücken  
Christine Vella, University of Malta  
David White, University of York  
Reinhard Wilhelm, Universität Saarbrücken  
Willem-Paul de Roever, Universität Kiel  
Robert de Simone, INRIA - Sophia Antipolis  
Reinhard v. Hanxleden, Universität Kiel  
Jan H. van Schuppen, CWI - Amsterdam

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