Computing Majority by Constant Depth Majority Circuits with Low Fan-in Gates*†

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Abstract

We study the following computational problem: for which values of k, the majority of n bits MAJ_n can be computed with a depth two formula whose each gate computes a majority function of at most k bits? The corresponding computational model is denoted by $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$. We observe that the minimum value of k for which there exists a $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ circuit that has high correlation with the majority of n bits is equal to $\Theta(n^{1/2})$. We then show that for a randomized $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ circuit computing the majority of n input bits with high probability for every input, the minimum value of k is equal to $n^{2/3+o(1)}$. We show a worst case lower bound: if a $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ circuit computes the majority of n bits correctly on all inputs, then $k \geq n^{13/19+o(1)}$. This lower bound exceeds the optimal value for randomized circuits and thus is unreachable for pure randomized techniques. For depth 3 circuits we show that a circuit with $k = O(n^{2/3})$ can compute MAJ_n correctly on all inputs.

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1 Introduction

In this paper we study majority functions and circuits consisting of them. These functions and circuits arise for various reasons in many areas of Computational Complexity (see e.g. [12, 14, 7]). In particular, the iterated majority function (or recursive majority) consisting of iterated application of majority of small number of variables to itself, turns out to be of great importance, helps in various constructions and provides an example of the function with interesting complexity properties in various models [8, 11, 13, 9].

One of the most prominent examples to illustrate this is the proof by Valiant [17] that the majority MAJ_n of n variables can be computed by a boolean circuit of depth $5.3 \log n$.

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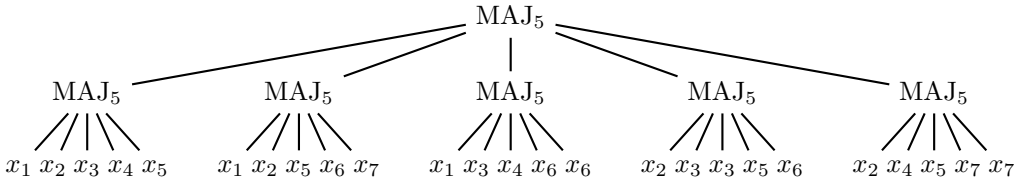
The construction of Valiant is randomized and there is no deterministic construction known achieving the same (or even reasonably close) depth parameter. The construction works as follows. Consider a uniform boolean formula (that is, tree-like circuit) consisting of $5.3 \log n$ interchanging layers of AND and OR gates of fan-in 2. For each input to the circuit substitute a random variable of the function MAJ_n. Valiant showed that this circuit computes MAJ_n with positive probability. Note that AND and OR gates are precisely MAJ_2 functions with different threshold values. Thus this construction can be viewed as a computation of MAJ_n by a circuit consisting of MAJ_2 gates. There are versions of this construction with the circuits consisting of MAJ_3 gates (see, e.g., [5]).

In this paper we study what happens with this setting if we restrict the depth of the circuit to a small constant. That is, we study for which k the function MAJ_n can be computed by small depth circuit consisting of MAJ_k gates. We mostly concentrate on depth 2 and denote the corresponding model by $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$. For example, the majority of $n = 7$ bits $x_1, x_2, \ldots, x_7$ can be computed with the following $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ circuit for $k = 5$:



We study which upper and lower bounds on k can be shown.

More context to the problem under consideration comes from the studies of boolean circuits of constant depth. The class $\widehat{\mathsf{TC}}^0$ of boolean functions computable by polynomial size constant depth circuits consisting of MAJ gates plays one of the central roles in this area. Its natural generalization is the class TC^0 in which instead of MAJ gates one can use arbitrary linear threshold gates, that is analogs of the majorities in which variables are summed up with arbitrary integer coefficients and are compared with arbitrary integer threshold. It is known that to express any threshold function it is enough to use exponential size coefficients. To show that TC^0 is actually the same class as $\widehat{\mathsf{TC}}^0$ it is enough to show that any linear threshold function can be computed by constant depth circuit consisting of threshold functions with polynomial-size coefficients (polynomial size can be simulated in $\widehat{\mathsf{TC}}^0$ by repetition of variables). It was shown by Siu and Bruck in [16] that any linear threshold function can be computed by polynomial size depth-3 majority circuit. This result was improved to depth-2 by Goldmann, Håstad and Razborov in [4]. More generally, it was shown in [4] that depth-d polynomial size threshold circuit can be computed by depth-$(d+1)$ polynomial size majority circuit, in particular establishing the class of depth-2 threshold circuits as one of the weakest classes for which we currently do not know superpolynomial size lower bounds. The best lower bound known so far is $\Omega(\frac{n^{3/2}}{\log^3 n})$ by Kane and Williams [10].

Note, however, that the result of [4] does not translate to monotone setting. Hofmeister in [6] showed that there is a monotone linear threshold function requiring exponential size depth-2 monotone majority circuit. Recently this result was extended by Chen, Oliveira and Servedio [2] to monotone majority circuits of arbitrary constant depth.

Our setting can be viewed as a scale down of the setting of [4] and [6]. In [4, 6] exponential weight threshold functions are compared to depth-2 threshold circuits with polynomial weights. In our setting we compare weight-n threshold functions with depth-2 threshold circuits with weights k. In this paper we consider monotone setting.

Another context to our studies comes from the studies of lower bounds against $\widehat{\mathsf{TC}}^0$. Allender and Koucký in [1] showed that to prove that some function is not in $\widehat{\mathsf{TC}}^0$ it is enough

to show that some self-reducible function requires circuit-size at least $n^{1+\varepsilon}$ when computed by constant depth majority circuit. As an intermediate result they show that MAJ_n can be computed by $O(1)$-depth circuit consisting of $\mathrm{MAJ}_{n^\varepsilon}$ gates and of size $O(n \log n)$. This setting is similar to ours, however in this paper we are interested in the precise depth and we do not pose additional bounds on the size of the circuit (however note that the bound on the fan-in k of the gates and the bound on the depth d of the circuit naturally imply the bound of $O(k^d)$ on the size of the circuit).

We consider three models of computation of the majority function: computation on most of the inputs (that is, high correlation with the function), randomized computation with small error probability on all inputs, and deterministic computation with no errors. We prove the following lower and upper bounds for our setting.

Circuits with high correlation. We observe that the minimum value of k for which there exists a $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ circuit that computes MAJ_n correctly on $2/3$ fraction of all the inputs, is equal to $\Theta(n^{1/2})$. A lower bound is proved by observing that a circuit with $k = \alpha n^{1/2}$ does not even have a possibility to read a large fraction of input bits when the constant α is small enough. We show that in this case the circuit errs on many inputs. An upper bound is proved for the following natural circuit: pick $k = \Theta(n^{1/2})$ random subsets of the n inputs bits of size k, compute the majority for each of them, and then compute the majority of results. Such a circuit computes MAJ_n correctly with high probability on inputs whose weight is not too close to $n/2$. By tuning the parameters appropriately, we ensure that the middle layers of the boolean hypercube (containing inputs where the circuits errs with high probability) constitute only a small fraction of all the inputs.

Randomized circuits. We prove that for a probabilistic distribution $\mathcal{C}$ of $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ circuits with a property that for every input $A \in \{0,1\}^n$ the probability that $\mathcal{C}(A) = \mathrm{MAJ}_n(A)$ is $1 - \varepsilon$ for a constant $\varepsilon > 0$, the minimum value of k is $n^{2/3}$, up to polylogarithmic factors. A lower bound is proved by showing that a small circuit must err on a large fraction of minterms/maxterms of MAJ_n. Roughly, the majority function have many inputs $A \in \{0,1\}^n$ with a property that changing a single bit in A changes the value of the function (these are precisely minterms and maxterms of MAJ_n). If k is small enough, a $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ circuit can reflect such a change in the value only for a small fraction of inputs. To show an upper bound, we split the n input bits into blocks and for each block compute several middle layers values of the bits of this block in sorted order. We then compute the majority of all the resulting values. We show that by tuning the parameters appropriately, one can ensure that this circuit err only on a polynomially small fraction of inputs.

Deterministic circuits. The trivial upper bound on k is $k \leq n$. We do not have any nontrivial upper bound on k for depth 2 circuits. We however have examples for $n = 7,\ 9,\ 11$ of circuits with $k = n - 2$. For depth 3 we have an upper bound $O(n^{2/3})$ which coincides with the optimal value for depth 2 randomized circuits up to polylogarithmic factor. We prove this upper bound by extending the construction of upper bound for depth 2 randomized circuits. We use an extra layer of the circuit to preorder the inputs. Regarding the lower bound for depth 2 we observe that the following simple special case cannot compute MAJ_n: each gate is a standard majority (that is, with threshold $k/2$) of exactly $k = n - 2$ distinct variables. Next, we proceed to the main result of the paper. We show that the minimum value of k for which there is a depth 2 circuit computing MAJ_n on all inputs is at least $n^{13/19}$ up to a polylogarithmic factor.

Note that this lower bound exceeds the optimal value of k for randomized circuits. Thus, despite the fact that randomized techniques is extensively used for studying majority and circuits constructed from it and proves to be very powerful (recall for example Valiant's result [17]), in our setting using combinatorial methods we prove a lower bound that is unreachable for a pure probabilistic approach. The proof of this result however is still probabilistic: in essence we consider a circuit with k smaller than $n^{13/19}$ and build a distribution on inputs that fools this circuit. The catch is that the distribution is tailored to fool this particular circuit: it is constructed via a non-trivial process that involves the values of the gates of the circuit on various inputs.

The rest of the paper is organized as follows. In Section 2 we give necessary definitions and collect technical statements. In Section 3 we study circuits computing the function with high correlation. In Section 4 we give bounds for randomized circuits. In Section 5 we study deterministic circuits. Finally, in Section 6 we give concluding remarks and state several open problems.

2 Definitions and Preliminaries

In this section we will give necessary definitions and collect technical statements that we will use throughout the paper.

We are going to study circuits computing the well known boolean majority function defined as follows: $\mathrm{MAJ}_n(x_1, x_2, \ldots, x_n) = [\sum_{i=1}^n x_i \geq n/2]$. Here, $[\cdot]$ denotes the standard Iverson bracket: for a predicate P, $[P] = 1$ if P is true, and $[P] = 0$ is P is false. To abuse notation, we will also use $[m]$ to denote the set $\{1, 2, \ldots, m\}$.

It will be convenient to use $X = \{x_1, x_2, \ldots, x_n\}$ for the set of n input bits. For an assignment $A\colon X \to \{0,1\}$, by $w(A)$ we denote the weight of A, that is, $\sum_{x\in X} A(x)$. For a subset of input variables $S \subseteq X$, by $w_S(A)$ we denote the weight of A on X: $w_S(A) = \sum_{x\in S} A(x)$. By $\mathrm{MAJ}_S(X)$ we denote the majority function on S: $\mathrm{MAJ}_S(X) = [\sum_{x\in S} x \geq |S|/2]$. In particular, MAJ_X is just MAJ_n.

An assignment $A\colon X \to \{0,1\}$ is called a minterm of MAJ_n if $\mathrm{MAJ}_n(A) = 1$, but flipping any 1 to 0 in A results in an assignment A' such that $\mathrm{MAJ}_n(A') = 0$. A maxterm is defined similarly with the roles of 0 and 1 interchanged.

The majority function is a special case of a threshold function: $f(X) = [\sum_{i=1}^n a_i x_i \geq t]$. For such a function f and an assignment $A\colon X \to \{0,1\}$, let difference of f w.r.t. A be $\mathrm{diff}(f, A) = \sum_{i=1}^n a_i A(x_i) - t$. In particular, $f(A) = 1$ iff $\mathrm{diff}(f, A) \geq 0$.

The $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ computational model that we study in this paper is defined as a depth two formula (we will call it a circuit also) consisting of arbitrary *threshold* gates of the form $[\sum c_i x_i \geq t]$ where c_i's are positive integers (this, in particular, means that the model is monotone) and $\sum c_i \leq k$. At the same time, abusing notation, by MAJ_n and MAJ_X we always mean the standard majority function. We note that the coefficients in c_i can be simulated by repetition of variables (note that k upper bounds the sum of the coefficients). So the generalization of the MAJ_k in the circuit compared to MAJ_n is that we allow arbitrary threshold. We note however, that if we are interested in the value of k up to a constant factor (which we usually do), it is not an actual generalization since any threshold can be simulated by substituting constants 0 and 1 as inputs to the circuit.

For a gate G at the bottom level of a $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ circuit, by $X(G)$ we denote the set of its input bits.

2.1 Tail Bounds and Binomial Coefficients Estimates

We will use the following versions of Chernoff–Hoeffding bound (see, e.g., [3]).

▶ **Lemma 1** (Chernoff–Hoeffding bound). *Let $Y = \sum_{i=1}^{m} Y_i$, where Y_i, $i \in [m]$, are independently distributed in $[0,1]$. Then for all $t > 0$, $\Pr[Y > E[Y] + t], \Pr[Y < E[Y] - t] \le e^{-2t^2/m}$. For all $\varepsilon > 0$, $\Pr[Y > (1+\varepsilon)E[Y]], \Pr[Y < (1-\varepsilon)E[Y]] \le e^{-\frac{\varepsilon^2}{3}E[Y]}$.*

We will also need the following well known estimates for the binomial coefficients (see, e.g., [15, Section 4.2]):

▶ **Lemma 2.** *The middle binomial coefficient is about $n^{1/2}$ times smaller than 2^n. To make it smaller than 2^n by arbitrary polynomial factor, it is enough to step away from the middle by about $\Theta(\sqrt{n \ln n})$ ($0 < c < 1$ is a constant below):*

$$\binom{n}{n/2} = \Theta(1) \cdot 2^n \cdot n^{-1/2} \quad \text{and} \quad \binom{n}{\frac{n}{2} + \frac{c\sqrt{n \ln n}}{2}} = \Theta(2^n n^{-\frac{1}{2}} n^{-\frac{c^2}{2}}). \tag{1}$$

2.2 Hypergeometric Distribution

The hypergeometric distribution is defined in the following way. Consider a set S of size m and its subset S' of size k. Select (uniformly) a random subset T of size t in S. Then a random variable $|T \cap S'|$ has a hypergeometric distribution. The values m, k and t are parameters here. We will need the following basic properties of this distribution.

▶ **Lemma 3.** *Suppose in hypergeometric distribution $k = k(m) \le m/2$ (that is, k may depend on m). Let $t = t(m)$ be a function with $\varepsilon m < t < (1-\varepsilon)m$ for some constant $0 < \varepsilon < 1$. Then, for any integer l, $\mathrm{Prob}(|T \cap S'| = l) = O(k^{-1/2})$, where $O(\cdot)$ is for $m \to \infty$ and the constant inside $O(\cdot)$ depends on ε, but does not depend on m, k and t. Moreover, if $|l - \frac{tk}{m}| = O(1)$, then this probability is in fact $\Theta(k^{-1/2})$.*

▶ **Lemma 4.** *Suppose in hypergeometric distribution $k = k(m) \le m/2$ (that is, k may depend on m). Let $t = t(m)$ be a function with $\varepsilon m < t < (1-\varepsilon)m$ for some constant $0 < \varepsilon < 1$. Consider an arbitrary antichain A on S' (that is, a family of subsets of S' none of which is a subset of some other). Then the probability $\Pr[T \cap S' \subseteq A] = O(k^{-1/2})$, where $O(\cdot)$ is for $m \to \infty$ and the constant inside $O(\cdot)$ depends on ε, but does not depend on m, k and t.*

▶ **Lemma 5.** *For S, S' and T as above we have $\mathrm{Prob}\{|T \cap S'| \ge l\} \le (tk/m)^l$.*

3 Circuits with High Correlation

In this section, we prove that the minimum value of k for which there exists a $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ circuit that computes MAJ_n correctly on, say, $2/3$ fraction of all the inputs, is equal to $\Theta(n^{1/2})$.

3.1 Upper Bound

▶ **Theorem 6.** *For any $\varepsilon > 0$, there exists a circuit C in $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$, where $k = O_\varepsilon(n^{1/2})$, that agrees with MAJ_n on at least $(1-\varepsilon)$ fraction of the boolean hypercube $\{0,1\}^n$.*

Proof Sketch. The required circuit is straightforward: we just pick k random subsets $S_1, S_2, \ldots, S_k$ of X of size k, compute the majority for each of them, and then compute the majority of the results: $C(X) = \mathrm{MAJ}_k(\mathrm{MAJ}_{S_1}(X), \mathrm{MAJ}_{S_2}(X), \ldots, \mathrm{MAJ}_{S_k}(X))$. The

resulting circuit has a high probability of error on middle layers of the boolean hypercube. We however select the parameters so that all the inputs from these middle layers constitute only a small $\varepsilon/2$ fraction. We then show that among all the remaining inputs (not belonging to middle layers) there is only a fraction $\varepsilon/2$ (of all the inputs) where MAJ_n may be computed incorrectly. Overall, this gives a circuit that errs on at most ε fraction of the inputs. ◀

3.2 Lower Bound

▶ **Theorem 7.** *Let C be a $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ circuit that computes MAJ_n correctly on a fraction $1-\varepsilon$ of all 2^n inputs for a constant $\epsilon \leq 1/3$. Then $k = \Omega_\varepsilon(n^{1/2})$.*

Proof Sketch. Let $k = \alpha n^{1/2}$ for a small enough constant $\alpha = \alpha(\varepsilon)$. Note that such a circuit can read at most $k^2 = \alpha^2 n$ of the input bits. This means that the circuit errs on a large number of inputs. ◀

4 Randomized Circuits

The upper bound from the previous section, however, is not enough to obtain a randomized circuit since the construction in Theorem 6 has a very high error probability on the middle layers of the boolean cube. By a randomized circuit here we mean a probabilistic distribution on deterministic circuits computing the function correctly on every input with high probability.

It is not difficult to see that the existence of a randomized circuit is equivalent to an existence of a deterministic circuit computing the function correctly on most of minterms and maxterms.

▶ **Lemma 8.** *If there exists a randomized circuit $\mathcal{C}$ in $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ computing MAJ_n with error probability ε, then there exists a deterministic circuit C in $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ computing MAJ_n incorrectly on at most ε fraction of minterms and maxterms. Conversely, if there exists a deterministic circuit C in $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ computing MAJ_n incorrectly on at most ε fraction of minterms and maxterms, then there exists a randomized circuit $\mathcal{C}$ in $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ computing MAJ_n with error probability at most 2ε.*

From now on instead of probabilistic circuits we study deterministic circuits with high accuracy on two middle layers of $\{0,1\}^n$.

4.1 Upper Bound

▶ **Theorem 9.** *There exists a randomized $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ circuit computing MAJ_n incorrectly on each input with probability at most $1/\operatorname{poly}(n)$ for $k = O(n^{2/3}\log^{1/2} n)$.*

Proof Sketch. Partition the set of n input bits into $n^{1/3}$ blocks of size $p = n^{2/3}$: $X = X_1 \sqcup X_2 \sqcup \ldots \sqcup X_{\frac{n}{p}}$. For each block X_i, compute $[\sum_{x \in X_i} x \geq m]$ for all $m \in [\frac{p}{2} - \frac{t}{2}, \frac{p}{2} + \frac{t}{2}]$ for $t \approx n^{1/3}\log^{1/2} n$, and return the majority of results. By selecting the right value of t, this gives a circuit that computes MAJ_n incorrectly only on a fraction $\frac{1}{\operatorname{poly}(n)}$ of inputs. ◀

4.2 Lower Bound

In this subsection we show that the upper bound of the previous subsection is essentially tight.

▶ **Theorem 10.** *If a $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ circuit computes MAJ_n on a $1-\varepsilon$ fraction of minterms and maxterms for $\varepsilon < 1/10$, then $k = \Omega(n^{2/3})$.*

Proof Sketch. The majority function have many inputs $A \in \{0,1\}^n$ with a property that changing a single bit in A changes the value of the function (these are precisely minterms and maxterms of MAJ_n). If $k = \alpha n^{2/3}$ for a small enough constant α, a $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ circuit can reflect such a change in the value only for a small fraction of inputs. ◀

5 Deterministic Circuits

In this section, we consider $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ circuits that compute MAJ_n correctly on all 2^n inputs.

5.1 Upper Bounds

5.1.1 Depth Two

In this section, we present $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ circuits computing MAJ_n on all inputs for $k = n-2$ when $n = 7, 9, 11$. These circuits were found by extensive computer experiments (with the help of SAT-solvers). Though the examples below look quite "structured", currently, we do not know how to generalize them to all values of n (not to say about constructing such circuits for sublinear values of k). In the examples below, we provide $k = n-2$ sequences consisting of $k = n-2$ integers from $[n]$. These are exactly the input bits of the k majority gates at the lower level of the circuit. That is, each gate computes the standard MAJ_k function (whose threshold value is $k/2$).

$n = 7$:

```
1 2 3 4 5
1 2 3 6 7
1 4 5 6 7
2 2 4 5 6
3 4 5 7 7
```

$n = 9$:

```
1 2 3 4 5 6 7
1 2 3 4 5 8 9
1 2 3 6 7 8 9
1 4 5 6 7 8 9
1 3 5 5 7 9 9
1 2 4 6 6 8 8
2 3 4 5 6 7 8
```

$n = 11$:

```
1 2 3 4 5 6 7  8  9
1 2 3 4 5 6 7 10 11
1 2 3 4 5 8 9 10 11
1 2 3 6 7 8 9 10 11
1 4 5 6 7 8 9 10 11
1 2 2 4 6 6 8 10 10
2 4 4 5 6 7 8 10 11
3 3 5 5 7 7 8  9 11
3 3 6 8 9 9 9 10 10
```

Note that in the examples above there is always a gate in the circuit having one variable repeated more than once. Next we observe that this is unavoidable for $k = n-2$.

▶ **Lemma 11.** *For odd n there is no $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ circuit for $k = n-2$ with all gates being standard majorities (that is, with the threshold $n/2$) and having exactly k distinct variables in each gate on the bottom level.*

5.1.2 Depth Three

In this section we extend the proof of the upper bound for randomized depth-2 circuits (Theorem 9) to construct a circuit of depth 3 for $k = O(n^{2/3})$ computing majority on all inputs.

▶ **Theorem 12.** *For $k = O(n^{2/3})$ there is a circuit of depth 3 computing majority of n variables on all inputs.*

Proof Sketch. We adopt the strategy of the proof of Theorem 9. That is, we break inputs into $O(n^{1/3})$ blocks, compute majorities on each block on middle $O(n^{1/3})$ layers and then

compute the majority of the results. We use the third layer of majority gates to induce additional structure on the inputs. ◀

5.2 Lower Bound

In this section we will extend the lower bound on k above $\Omega(n^{2/3})$ for depth-2 circuits computing MAJ_n on all inputs.

▶ **Theorem 13.** *Suppose a* $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ *circuit computes* MAJ_n *on all inputs. Then* $k = \Omega\left(n^{13/19} \cdot (\log n)^{-2/19}\right)$.

We also show the following result for the special case of circuits with bounded weights.

▶ **Theorem 14.** *Suppose a* $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ *circuit computes* MAJ_n *on all inputs and uses only weights at most* W *in the gates. Then* $k = \Omega(n^{7/10} \cdot (\log n)^{-1/5} \cdot W^{-3/10})$.

In particular, we get the following corollary for circuits with unweighted gates.

▶ **Corollary 15.** *Suppose a* $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ *circuit computes* MAJ_n *on all inputs and each variable occurs in each gate of the bottom level at most once. Then* $k = \Omega(n^{7/10} \cdot (\log n)^{-1/5})$.

The rest of this section is devoted to the unified proof of these lower bounds. To follow this proof it is convenient to think that $k = n^{\frac{2}{3}+\varepsilon}$ for some small $\varepsilon > 0$. In the end it will indeed be the case up to a logarithmic factor. In the proof we will calculate everything precisely in terms of parameters n and k, but we will provide estimates assuming that $k = n^{2/3+\varepsilon}$. This is done in order to help the reader to follow the proof.

Let F be a $\mathrm{MAJ}_k \circ \mathrm{MAJ}_k$ formula computing MAJ_n on all inputs from $\{0,1\}^n$. Denote by W the largest weight of a variable in gates of F.

5.2.1 Normalizing a formula

We start by "normalizing" F, that is, removing some pathological gates from F. We do this in two consecutive stages.

Stage 1: removing AND-like gates. We will need that no gate can be fixed to 0 by assigning a small number of variables to 0 (here and in what follows we consider gates from the bottom level only). For this, assume that there is a gate that can be fixed to 0 by assigning to 0 less than $n/(100k) = n^{1/3-\varepsilon}/100$ variables. Take these variables and substitute them by 0; this kills this gate (and might potentially introduce new gates with the property). We repeat this process until there are no bad gates left. Recall that the number of gates at the bottom level is at most $k = n^{2/3+\varepsilon}$, so there are at most $k = n^{2/3+\varepsilon}$ steps in this process and hence n is replaced by $99n/100$. To simplify the presentation, we just assume that $|X| = n$ and that F has no bad gates.

Stage 2: removing other pathological gates and variables. The formula F contains at most $k^2 = n^{\frac{4}{3}+2\varepsilon}$ occurrences of variables (counting with multiplicities). Let $x^* \in X$ be a least frequent variable at the leaves. The number of occurrences of x^* is at most $k^2/n = n^{1/3+2\varepsilon}$. In the following we consider only assignments A with $\mathrm{diff}(\mathrm{MAJ}_n, A) = -1$ setting x^* to 0:

$$\mathcal{A}^* = \{A\colon X \to \{0,1\} \mid \mathrm{diff}(\mathrm{MAJ}_n, A) = -1 \text{ and } A(x^*) = 0\}.$$

We also focus on the gates from the first level that depend on x^*, denote this set by $\mathcal{G}^*$ (hence $|\mathcal{G}^*| \le k^2/n = n^{1/3+2\varepsilon}$). The total number of variables in the gates from $\mathcal{G}^*$ (counting with multiplicities) is at most $k|\mathcal{G}^*| \le k^3/n = n^{1+3\varepsilon}$.

We now additionally normalize the circuit. We get rid of the following bad gates and variables:

1. gates in $\mathcal{G}^*$ that can be assigned to 1 by fixing less than $n^2/(100k^2) = n^{2/3-2\varepsilon}/100$ variables in $X \setminus \{x^*\}$ to 1;
2. gates in $\mathcal{G}^*$ with the weight of the variable x^* greater than $100k^3/n^2 = 100n^{3\varepsilon}$;
3. variables with total weight in all gates in $\mathcal{G}^*$ greater than $100k^3/n^2 = 100n^{3\varepsilon}$.

We do this by the following iterative procedure. If on some step we have a gate violating 1 we fix less than $n^2/(100k^2) = n^{2/3-2\varepsilon}/100$ variables of the gate among $X \setminus \{x^*\}$ to 1 to assign the gate to a constant. If we have a gate violating 2 we fix all the variables of the gate among $X \setminus \{x^*\}$ to 1 to assign the gate to a constant. If we have a variable violating 3, we fix the violating variable to 1.

We note that if we fix all variables in $G \in \mathcal{G}^*$ except x^* to 1, then the gate becomes constant. Indeed, if it is not constant, then the gate outputs 0 on the input with $x^* = 0$ and the rest of the variables equal to 1. Due to the monotonicity of the gate this means that the gate can be assigned to 0 by assigning a single variable x^* to 0 and we got rid of the gates with this property on the first stage of the normalization.

Since there are at most $k^2/n = n^{1/3+2\varepsilon}$ gates in $\mathcal{G}^*$ we will fix at most $n/100$ variables for case 1. Since the total weight of x^* is at most $k^2/n = n^{1/3+2\varepsilon}$ we will have case 2 at most $n/(100k) = n^{1/3-\varepsilon}/100$ times. Since each gate has at most $k = n^{2/3+\varepsilon}$ variables we will fix at most $n/100$ variables for the second case. Since the total weight of all variables in $\mathcal{G}^*$ is at most $k^3/n = n^{1+3\varepsilon}$ we will fix at most $n/100$ of them for the case 3.

In particular, we have fixed all variables having weight greater than $100k^3/n^2 = 100n^{3\varepsilon}$ in some gate of $\mathcal{G}^*$, so from now on we can assume that $W \leq 100k^3/n^2$.

Another important observation is that now in each gate there are at least $n^2/(100k^2)$ inputs. Otherwise the gate falls under condition of case 1 above.

After this normalization n is replaced by $97n/100$. To simplify the presentation, again, we assume that $|X| = n$ and the circuit F is normalized. Note that after redefining n the threshold of the function MAJ_n we are computing is no longer $n/2$, but rather is cn for some constant c close to $1/2$. This does not affect the computations in the further proof.

5.2.2 Analysis

The key idea is that if we have an assignment $A \in \mathcal{A}^*$ with $\mathrm{diff}(\mathrm{MAJ}_n, A) = -1$, then there is a gate $G \in \mathcal{G}^*$ with $-W \leq \mathrm{diff}(G, A) \leq -1$. Indeed, otherwise we can flip the variable x^*, the value of MAJ_n changes, but none of the gates changes their value. The plan of the proof is to construct an assignment that violates this condition. This will lead to a contradiction.

For an assignment $A \in \mathcal{A}^*$ with $\mathrm{diff}(\mathrm{MAJ}_n, A) = -1$ and integer parameters s and d (to be chosen later), consider the following process $\texttt{walk}(A, s, d)$.

$A_0 \leftarrow A$
for $i = 1$ to s **do**
  if for each $G \in \mathcal{G}^*$, $\mathrm{diff}(G, A_{i-1}) \notin \{-d, -d+1, \ldots, -1\}$ **then**
    stop the process
  else
    $G_i \leftarrow$ any gate from $\mathcal{G}^*$ such that $-d \leq \mathrm{diff}(G, A_{i-1}) < 0$
    $X_i \leftarrow$ set of variables G_i depends on that are assigned 1 by A_{i-1}
    $y_i \leftarrow$ a uniform random variable from X_i
    $A_i \leftarrow$ assignment to X resulting from flipping the value of y_i in A_{i-1}
  end if
end for

Clearly, this process decreases the weight of the initial assignment A by 1 at each iteration, for at most s iterations. In particular, $w(A) - w(A_i) = i$. We now consider three cases.

Case 1. *There exists an assignment $A \in \mathcal{A}^*$ with* $\mathrm{diff}(\mathrm{MAJ}_n, A) = -1$ *such that* `walk`(A, s, d) *stops after less than s iterations for some choices of random bits. This means that after $t < s$ iterations, for all the gates G in $\mathcal{G}^*$ we have that either* $\mathrm{diff}(G, A_t) < -d$, *or* $\mathrm{diff}(G, A_t) \geq 0$.

We select randomly a subset T of t variables from $Z = \{x \in X \setminus \{x^*\} \colon A_t(x) = 0\}$ and flip them. Denote the resulting assignment by A'. Clearly, $w(A) = w(A')$ and so $\mathrm{diff}(\mathrm{MAJ}_n, A') = -1$. Therefore there must be a gate G in $\mathcal{G}^*$ such that $-W \leq \mathrm{diff}(G, A') < 0$. Thus, before flipping t random variables, all the gates with negative difference has difference less than $-d$, while after the flipping, at least one gate G has difference at least $-W$. Let $Z' = \{x \in X(G) \setminus \{x^*\} \colon A_t(x) = 0\}$. This means that the flipping changed the values of at least $r = (d - W)/W$ variables of G, that is, $|T \cap Z'| \geq r$.

Let p be the probability that $|T \cap Z'| \geq r$ where the probability is taken over the random choice of T. By choosing the parameters s and d we will make p small enough so that with non-zero probability no gate from $\mathcal{G}^*$ satisfies this. Due to the discussion above this leads to a contradiction since flipping x^* changes the value of the function, but not the value of the circuit. The probability that no gate from $\mathcal{G}^*$ satisfies $|T \cap Z'| \geq r$ is at least $1 - |\mathcal{G}^*|p$. The probability p can be upper bounded using Lemma 5: $p \leq \left(\frac{t|Z'|}{|Z|}\right)^r \leq \left(\frac{sk}{n/2}\right)^r$ where the second inequality follows since $t < s$, $|Z'| \leq k$ and $|Z| \geq \frac{n}{2}$.

We want the probability $1 - |\mathcal{G}^*|p$ to be positive. Since $|\mathcal{G}^*| \leq k^2/n = n^{1/3+2\varepsilon}$ we get the following inequality on s, d, and k: $(k^2/n) \cdot (2sk/n)^r < 1$. We can satisfy this if $sk < n/4$ and $r \geq \log \frac{k^2}{n}$. Since $\log n > \log \frac{k^2}{n}$ for the latter it is enough to have $d = W \log n$. Overall, this case poses the following constraint for the considered parameters:

$$sk \leq n/4\,. \tag{2}$$

Case 2. *For each assignment $A \in \mathcal{A}^*$ (i.e.,* $\mathrm{diff}(\mathrm{MAJ}_n, A) = -1$*) the process* `walk`$(A, s, d)$ *goes through all s iterations for all choices of random bits.* We consider two subcases here.

Case 2.1. *For each assignment $A \in \mathcal{A}^*$ (i.e.,* $\mathrm{diff}(\mathrm{MAJ}_n, A) = -1$*) there exists a choice of variables $y_1, \ldots, y_s$ at line 8 of the process* `walk`(A, s, d)*, such that for each gate $G \in \{G_1, \ldots, G_s\}$ (recall that the gates $G_1, \ldots, G_s$ are selected at line 6 of the process) we have* $\mathrm{diff}(G, A) \leq f$*, where f is again a positive parameter to be chosen later.*

We estimate the expected number E of gates G from $\mathcal{G}^*$ that have $-d \leq \mathrm{diff}(G, A) \leq f$ where the expectation is taken over the random choices of A. Note that a particular gate $G \in \mathcal{G}^*$ may appear in the sequence $G_1, \ldots, G_s$ at most d times: the first time it appears, it must have $\mathrm{diff}(G, A_1) \leq -1$ for the current assignment A_1, the next time it has $\mathrm{diff}(G, A_2) \leq -2$ for the new current assignment A_2, and so on. If $Ed < s$ we get a contradiction: take an assignment $A \in \mathcal{A}^*$ with $\mathrm{diff}(\mathrm{MAJ}_n, A) = -1$ such that the number of gates G in $\mathcal{G}^*$ with $-d \leq \mathrm{diff}(G, A) \leq f$ is at most E, then we cannot have that for all of $G_1, \ldots, G_s$ it is true that $-d \leq \mathrm{diff}(G_i, A) \leq f$, there are just not enough gates with this diff.

Now we upper bound E. Due to the normalization stage any fixed gate has at least $n^2/(100k^2) = n^{2/3-2\varepsilon}/100$ variables in it. Note that the set of inputs B to the gate G that give $\mathrm{diff}(G, B) = i$ for any i form an antichain. Then due to Lemma 4 the probability for a gate to attain a certain value is at most $O(k/n) = O(1/n^{1/3-\varepsilon})$.

Hence

$$E \leq |\mathcal{G}^*| \cdot (f+d) \cdot O\left(\frac{k}{n}\right) = \frac{k^2}{n} \cdot (f+d) \cdot O\left(\frac{k}{n}\right) = O\left(\frac{k^3(f+d)}{n^2}\right) = O\left(\frac{k^3 f}{n^2}\right),$$

where for the last equality we add the constraint

$$d = O(f). \tag{3}$$

Overall, this case poses the following constraint for the parameters:

$$O\left(\frac{k^3 f d}{n^2}\right) = O(f d n^{3\varepsilon}) < s. \tag{4}$$

Case 2.2. *There exists an assignment $A \in \mathcal{A}^*$ (i.e.,* $\mathrm{diff}(\mathrm{MAJ}_n, A) = -1$*) such that for any choice of variables $y_1, \ldots, y_s$, for at least one gate $G \in \{G_1, \ldots, G_s\}$ we have* $\mathrm{diff}(G, A) > f$.

Fix a gate $G \in \mathcal{G}^*$ with $\mathrm{diff}(G, A) > f$. We are going to upper bound the probability (over the random choices of variables $y_1, \ldots, y_s$) that G appears among $G_1, \ldots, G_s$ during the process. If this probability is less than $1/k$, then by the union bound with a positive probability no gate such gate appears among $G_1, \ldots, G_s$ which leads to a contradiction with the case statement.

For G to appear among $G_1, \ldots, G_s$, the process has to select a variable appearing in G at line 8 many times. Indeed, if G appears in the process, then its diff with the current assignment is negative. At the same time, in the beginning of the process $\mathrm{diff}(G, A) > f$. Each time when the process reduces a variable at line 8 (that is, changes its value from 1 to 0), the value of the linear function computed at G decreases by at most W (just because W is the maximum weight of a variable in all the gates in $\mathcal{G}^*$). Thus, it is enough to upper bound the probability that for a fixed gate $G \in \mathcal{G}^*$ with $\mathrm{diff}(G, A) > f$, the process selects a variable from $X(G)$ at least f/W times.

Let $Y_1, \ldots, Y_s$ be random 0/1-variables defined as follows: $Y_i = 1$ iff the i-th reduced variable appears in G (i.e., $y_i \in X(G)$). Let $Y = \sum_{i=1}^{s} Y_i$. Our goal is to upper bound $\mathrm{Prob}(Y \geq f/W)$.

Let $H_1, \ldots, H_l$ be all the gates that share at least one variable with G. Assume that on step j we reduce a variable from H_i. Then

$$\mathrm{Prob}(Y_j = 1) = \mathrm{Prob}(y_i \in X(G)) = \frac{|X(G) \cap X(H_i)|}{|\{x \in X(H_i) \colon A_{j-1}(x) = 1\}|}.$$

Due to the stage 2.1 of the normalization process, $|\{x \in X(H_i) \colon A_{j-1}(x) = 1\}| \geq \frac{n^2}{100k^2} - d$. To see this, assume the contrary. Recall that $-d \leq \mathrm{diff}(H_i, A_{j-1}) < 0$. This means that by increasing at most d variables (i.e., changing their values from 0 to 1) from $X(H_i)$ in A_{j-1} results in an assignment of weight at most $\frac{n^2}{100k^2}$ that sets H_i to 1. This, in turn, contradicts to the fact that the circuit is normalized. Thus,

$$\mathrm{Prob}(Y_j = 1) \leq \frac{|X(G) \cap X(H_i)|}{\frac{n^2}{100k^2} - d} \leq \frac{|X(G) \cap X(H_i)|}{\frac{n^2}{200k^2}},$$

where we add a constraint

$$d \leq \frac{n^2}{200k^2}. \tag{5}$$

We are now going to use the fact that variables from a fixed gate H_i can be reduced at most d times. We upper bound $Y = \sum_{i=1}^{s} Y_i$ by the following random variable: $Z = \sum_{i=1}^{l} \sum_{j=1}^{d} Z_{ij}$. where each Z_{ij} is a random 0/1-variable such that

$$\text{Prob}(Z_{ij} = 1) = \frac{|X(G) \cap X(H_i)|}{\frac{n^2}{200k^2}},$$

and Z_{ij} are independent. That is, instead of reducing variables in some of H_i's in some random order, we reduce d variables in each H_i. Thus we reduce maximal possible number of variables in all gates. Clearly, for any r we have $\text{Prob}(Y \geq r) \leq \text{Prob}(Z \geq r)$.

Let us bound the expectation of Z. Since due to the normalization each variable of G appear in other gates at most $100k^3/n^2 = 100n^{3\varepsilon}$ times, we have

$$\sum_{i,j} |X(G) \cap X(H_i)| \leq d \cdot (100k^3/n^2) \cdot |X(G)| \leq 100 \cdot d \cdot k^4/n^2 = 100 \cdot n^{2/3+4\varepsilon} \cdot W \cdot \log n.$$

Overall we get $EZ \leq \frac{100dk^4/n^2}{n^2/200k^2} = 4 \cdot 10^4 \cdot d\frac{k^6}{n^4} = 4 \cdot 10^4 \cdot n^{6\varepsilon} \cdot W \cdot \log n$. Application of Chernoff–Hoeffding bound (Lemma 1) immediately implies that the probability that Z is twice greater than the expectation is exponentially small in $d \cdot \frac{k^6}{n^4}$. Since $d \cdot \frac{k^6}{n^4} = W \cdot \log n \cdot n^{9\varepsilon}$ grows asymptotically faster than $\log n$ for sure, we conclude that $\text{Prob}(Z \geq 2 \cdot EZ) < \frac{1}{n} \leq \frac{1}{k}$. Hence, if $f/W \geq 2 \cdot EZ$, then $\text{Prob}(Y \geq f/W) \leq \text{Prob}(Z \geq 2 \cdot EZ) < \frac{1}{k}$ as desired. Overall, this gives us the following constraint:

$$f \geq 4 \cdot 10^4 \cdot d \cdot W \cdot \frac{k^6}{n^4} = 4 \cdot 10^4 \cdot n^{9\varepsilon} \cdot W^2 \cdot \log n\,. \tag{6}$$

5.2.3 Tuning the parameters

It remains to set the parameters so that the inequalities (2)–(6) are satisfied and k is as large as possible. The inequality (4) sets a lower bound on s in terms of f, while (6) sets a lower bound on f. Putting them together gives a lower bound on s: $s \geq 4 \cdot 10^4 \cdot \frac{k^9}{n^6} \cdot W^3 \cdot \log^2 n\,.$ Combining it with the upper bound on s from (2), we can set the following equality on k and n: $\frac{n}{4k} = 4 \cdot 10^4 \cdot \frac{k^9}{n^6} \cdot W^3 \cdot \log^2 n$. Thus $k = \Omega\left(\frac{n^{7/10}}{(\log n)^{1/5} W^{3/10}}\right)$ and it is easy to see that with this k we can pick other parameters to satisfy all the constraints (we set f so that (6) turns into an equality, the inequalities (3) and (5) are satisfied since $W \leq \frac{k^3}{n^2}$).

This gives a proof of Theorem 14. For $W = 1$ we get $k = n^{7/10} \cdot (\log n)^{-1/5}$, which gives a proof for Corollary 15. For unbounded W recall that we can assume $W \leq \frac{k^3}{n^2}$ and thus $k = n^{13/19} \cdot (\log n)^{-2/19}$ and Theorem 13 follows.

6 Conclusion and Open Problems

The most interesting question left open is whether one can prove non-trivial upper bounds for k in the worst case. Currently, we do not know how to construct $\text{MAJ}_k \circ \text{MAJ}_k$ circuits computing MAJ_n on all inputs even for $k = n - 2$ (though we have many examples of such circuits for $n = 7, 9, 11$), not to say about $k = n^{\varepsilon}$ for $\varepsilon < 1$.

Another natural open question is to get rid of the logarithmic gap between upper and lower bound for depth-2 randomized circuits.

A natural direction is to extend our studies to the case of non-monotone $\text{MAJ}_k \circ \text{MAJ}_k$ circuits.

Many of our results naturally translate to larger depth circuits. Indeed, note that in the proofs of lower bounds we do not use the fact that the function on the top of the circuit is

majority. In these proofs it can be any monotone function. Thus we can split a depth-d circuit consisting of MAJ_k into two parts: bottom layer and the rest of the circuit. Then our lower bounds translate to this setting straightforwardly. It is interesting to proceed with the studies of larger depth majority circuits.

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