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Contents

Preface
João Bispo, Henri-Pierre Charles, Stefano Cherubin, and Giuseppe Massari ....... 0:ix

Invited Papers
ByteNite: A New Business Model for Grid Computing
Fabio Caironi and Niccolò Andrea Castelli .......................... 1:1–1:12
Challenges and Opportunities in C/C++ Source-To-Source Compilation
João Bispo, Nuno Paulino, and Luís Miguel Sousa ....................... 2:1–2:15
RUST-Encoded Stream Ciphers on a RISC-V Parallel Ultra-Low-Power Processor
Francesco Barchi, Giacomo Pasini, Emanuele Parisi, Giuseppe Tagliavini,
Andrea Bartolini, and Andrea Acquaviva ................................ 3:1–3:12

Regular Papers
An Evaluation of the State-Of-The-Art Software and Hardware Implementations
of BIKE
Andrea Galimberti, Gabriele Montanaro, William Fornaciari, and Davide Zoni ... 4:1–4:12
MonTM: Monitoring-Based Thermal Management for Mixed-Criticality Systems
Marcel Mettler, Martin Rapp, Heba Khdr, Daniel Mueller-Gritschneder,
Jörg Henkel, and Ulf Schlichtmann ........................................ 5:1–5:12
Dynamic Power Consumption of the Full Posit Processing Unit: Analysis and
Experiments
Michele Piccoli, Davide Zoni, William Fornaciari, Giuseppe Massari,
Marco Cococcioni, Federico Rossi, Sergio Saponara, and Emanuele Ruffaldi ...... 6:1–6:11
Adjacent LSTM-Based Page Scheduling for Hybrid DRAM/NVM Memory
Systems
Manolis Katsaragakis, Konstantinos Stavrakakis, Dimosthenis Masouros,
Lazaros Papadopoulos, and Dimitrios Soudris .......................... 7:1–7:12
**Preface**

This volume collects the proceedings of the PARMA-DITAM workshop 2023. PARMA-DITAM brings together the decade-long experience of two workshops: the workshop on Parallel Programming and Run-Time Management Techniques for Many-core Architectures (PARMA) and the workshop on Design Tools and Architectures for Multicore Embedded Computing Platforms (DITAM). These events first joined in 2014 and since then they represented a reference point in the European community of high-performance computer architectures, embedded systems and compiler technologies. PARMA-DITAM is co-located with and sponsored by the HiPEAC conference, which annually gathers the most excellent researchers on High Performance Embedded Architectures and Compilers within the European borders and beyond.

The PARMA-DITAM 2023 workshop includes topics such as parallel programming models, design space exploration tools and run-time management techniques aiming at exploring the features and performance of different computing architectures, possibly heterogeneous, (re-)programmable and/or (re-)configurable, spanning from embedded and cyber-physical systems, to high performance computing platforms.

This edition features 4 regular papers, carefully selected among 6 submissions by our expert Technical Program Committee after a double-blind review process. The editors are proud to propose, in the early pages of this volume, 3 additional manuscripts from invited research groups, who presented their research and results in invited talks during the workshop event.

The PARMA-DITAM workshop focuses on seven main topics:

- Parallel programming models and languages, compilers and virtualization techniques
- Runtime modelling, monitoring, adaptivity, and management
- Runtime trade-off execution, power management, and memory management
- Heterogeneous and reconfigurable many-core: architectures and design space exploration
- Methodologies, design tools, and high level synthesis for many-core architectures
- Parallel applications for many-core platforms
- Case studies, success stories and applications applying T1–T6

The editors invites researchers to submit their future works for consideration in the subsequent editions of this workshop.

João Bispo, Henri-Pierre Charles, Stefano Cherubin, and Giuseppe Massari