Two-Page Book Embeddings of 4-Planar Graphs*

Michael A. Bekos¹, Martin Gronemann², and Chrysanthi N. Raftopoulou³

- 1 Wilhelm-Schickhard-Institut für Informatik, Universität Tübingen, Germany bekos@informatik.uni-tuebingen.de
- 2 Institut f\u00fcr Informatik, Universit\u00e4t zu K\u00f6ln, Germany gronemann@informatik.uni-koeln.de
- 3 School of Applied Mathematical & Physical Sciences, NTUA, Greece crisraft@mail.ntua.gr

Abstract

Back in the eighties, Heath [7] showed that every 3-planar graph is subhamiltonian and asked whether this result can be extended to a class of graphs of degree greater than three. In this paper we affirmatively answer this question for the class of 4-planar graphs. Our contribution consists of two algorithms: The first one is limited to triconnected graphs, but runs in linear time and uses existing methods for computing hamiltonian cycles in planar graphs. The second one, which solves the general case of the problem, is a quadratic-time algorithm based on the book embedding viewpoint of the problem.

1998 ACM Subject Classification G.2.2 Graph Theory

Keywords and phrases Book Embedding, Subhamiltonicity, 4-Planar Graphs

Digital Object Identifier 10.4230/LIPIcs.STACS.2014.137

1 Introduction

Book embeddings have a long history and arise in various application areas such as VLSI design [5]. In a book embedding the placement of nodes is restricted to a line, the spine of the book. The edges are assigned to different pages of the book. A page can be thought of as a half-plane bounded by the spine where the edges are drawn as circular arcs between their endpoints. We say that a graph admits a k-page book embedding if one can assign the edges to k pages and there exists a linear ordering of the nodes on the spine such that no two edges of the same page cross. The minimum number of pages required to construct such an embedding is the book thickness or page number of a graph and has received much attention in the past. Yannakakis [14] describes a linear-time algorithm to embed every planar graph into a book of four pages. Bernhart et al. [2] show that a graph is two-page embeddable iff it is subhamiltonian. A subhamiltonian graph is a subgraph of a planar hamiltonian graph. It is NP-complete to determine whether a graph is subhamiltonian [13]. Often referred to as augmented hamiltonian cycle, a subhamiltonian cycle is a cyclic sequence of nodes in a graph that would form a hamiltonian cycle when adding the missing edges without destroying planarity. The relation between subhamiltonian cycles and two-page book embeddings is

^{*} Work on this problem began at Dagstuhl Seminar 13151. We thank the organizers, participants and Prof. Dr. M. Kaufmann. The work of M.A. Bekos is implemented within the framework of the Action "Supporting Postdoctoral Researchers" of the Operational Program "Education and Lifelong Learning" (Action's Beneficiary: General Secretariat for Research and Technology), and is co-financed by the European Social Fund (ESF) and the Greek State.

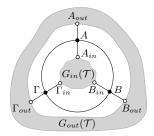


Figure 1 Triangle \mathcal{T} separating $G_{in}(\mathcal{T})$ and $G_{out}(\mathcal{T})$ on removal.

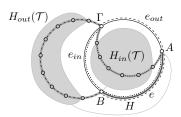


Figure 2 Merging $H_{in}(\mathcal{T})$ (dotted) and $H_{out}(\mathcal{T})$ (dashed) into H (bold gray).

quite intuitive. The order of the nodes on the spine is equivalent to the cyclic order of the subhamiltonian cycle. The edges are partitioned by whether they lie in the interior of the cycle or not.

An early result is due to Whitney [12], who proves that every maximal planar graph with no separating triangles is hamiltonian. Tutte [11] shows that every 4-connected planar graph has a hamiltonian cycle. Chiba et al. [4] provide a linear-time algorithm to find a hamiltonian cycle in a 4-connected planar graph. Chen [3] gives a proof that every maximal planar graph with at least five vertices and no separating triangles is 4-connected. Sanders [10] generalizes a theorem of Thomassen and shows that any 4-connected planar graph has a hamiltonian cycle that contains two arbitrarily chosen edges of the graph. Based on Whitney's theorem, Kainen et al. [9] show that every planar graph with no separating triangles is subhamiltonian. Another result is by Chen [3] who shows that if a maximal planar graph contains only one such triangle, then it is hamiltonian. Helden [8] improves this result further to two triangles. The aforementioned results are all related to the problem of embedding planar graphs into two pages. However, there is an extensive amount of literature on embedding various types of graphs into books; see e.g. [6]. One result that is interesting in our context is that of Heath [7], who describes a linear-time algorithm to embed any 3-planar graph into two pages.

We study the problem of embedding 4-planar graphs into books with two pages. We tackle this problem from two sides. The first approach is restricted to triconnected graphs (Section 2) but builds on existent results and is therefore of a simple nature compared to the second approach. Extending it to biconnected graphs is not straightforward, though. The algorithm of Section 3 —which is less efficient in terms of time complexity— exploits the degree restriction to construct a two-page book embedding. Due to space constraints, some of our proofs are only sketched, omitted details are given in [1].

2 Subhamiltonicity of Triconnected 4-Planar Graphs

In this section, we first investigate properties of separating triangles in 4-planar graphs and then we use those to derive a solution for a single separating triangle. Unlike Chen [3] and Helden [8], we are able to extend our approach to an unbounded number of triangles by exploiting the degree restriction. We say a subhamiltonian cycle H crosses a face if there are two consecutive vertices in H that are incident to the face but not adjacent to each other.

▶ Lemma 1. Every triconnected planar graph with no separating triangles has a subhamiltonian cycle that crosses every face at most once and it can be computed in linear time.

Proof. In the triconnected case, Kainen et al. [9] construct a maximal planar graph G' = (V', E') by inserting a vertex into each non-triangular face of G and connect it to the vertices

of that face. Clearly, this takes linear time and G' is 4-connected. We can use the linear-time algorithm of Chiba et al. [4] to obtain a hamiltonian cycle H' for G'. Deleting the vertices of V' - V yields a subhamiltonian cycle H for G that crosses each face at most once.

Given an embedded triconnected 4-planar graph G with a fixed outerface and a separating triangle \mathcal{T} with vertices $V(\mathcal{T}) = \{A, B, \Gamma\}$, we denote the subgraph of G contained in \mathcal{T} by $G_{in}(\mathcal{T})$ and the subgraph of G outside \mathcal{T} by $G_{out}(\mathcal{T})$. We also denote $\overline{G}_{in}(\mathcal{T}) = G - G_{out}(\mathcal{T})$ and $\overline{G}_{out}(\mathcal{T}) = G - G_{in}(\mathcal{T})$. Since G is triconnected and 4-planar, every vertex of \mathcal{T} has degree four and is adjacent to exactly one vertex in $G_{in}(\mathcal{T})$ and $G_{out}(\mathcal{T})$, respectively. We denote these with $A_{in}, B_{in}, \Gamma_{in}$ and $A_{out}, B_{out}, \Gamma_{out}$, respectively (see Fig. 1).

- ▶ Lemma 2. Given a 4-planar triconnected graph G and a separating triangle $\mathcal{T} = \{A, B, \Gamma\}$, then $A_{in}, B_{in}, \Gamma_{in}(A_{out}, B_{out}, \Gamma_{out})$ are pairwise distinct or all represent the same vertex.
- **Proof.** In the other case, where w.l.o.g. $A_{in} = B_{in} = v$ and $\Gamma_{in} \neq v$, there exists a separation pair (v, Γ) contradicting the triconnectivity of G. A symmetric argument applies to $A_{out}, B_{out}, \Gamma_{out}$.
- ▶ **Lemma 3.** In a 4-planar triconnected graph, every pair of distinct separating triangles \mathcal{T} and \mathcal{T}' is vertex disjoint, i.e. $V(\mathcal{T}) \cap V(\mathcal{T}') = \emptyset$.
- **Proof.** Assume to the contrary that \mathcal{T} and \mathcal{T}' share an edge or a vertex. In the first case, let w.l.o.g. e = (u, v) be the common edge. The degree of both u and v is at least five, since three edges are required for $\mathcal{T}, \mathcal{T}'$ and two additional edges to connect $G_{in}(\mathcal{T})$ and $G_{in}(\mathcal{T}')$ to \mathcal{T} and \mathcal{T}' , respectively. In the second case, let v denote the common vertex. Since v is part of two edge disjoint cycles and connected to $G_{in}(\mathcal{T})$ and $G_{in}(\mathcal{T}')$, it follows that $deg(v) \geq 6$.

Consider now a 4-planar triconnected graph with a single separating triangle \mathcal{T} . Similar to Chen [3], the idea is to compute two cycles $H_{in}(\mathcal{T})$ and $H_{out}(\mathcal{T})$ for $\overline{G}_{in}(\mathcal{T})$ and $\overline{G}_{out}(\mathcal{T})$ and link them via the separating triangle together. The crucial observation is that if two cycles intersect as illustrated in Fig. 2, i.e., they contain two edges of the triangle but have only one of them in common, then we can always merge them into one cycle.

- ▶ Lemma 4. Let G be a triconnected 4-planar graph, \mathcal{T} a separating triangle, and $H_{in}(\mathcal{T})$ and $H_{out}(\mathcal{T})$ two subhamiltonian cycles for $\overline{G}_{in}(\mathcal{T})$ and $\overline{G}_{out}(\mathcal{T})$, resp. If $E(H_{in}(\mathcal{T})) \cap E(\mathcal{T}) = \{e_{in}, e\}$ and $E(H_{out}(\mathcal{T})) \cap E(\mathcal{T}) = \{e_{out}, e\}$ where $\{e, e_{in}, e_{out}\}$ are the edges of \mathcal{T} , then G is subhamiltonian.
- **Proof.** Let w.l.o.g. e = (A, B), $e_{in} = (B, \Gamma)$ and $e_{out} = (A, \Gamma)$ as illustrated in Fig. 2. The result of removing the edges of \mathcal{T} from both cycles are two paths $P_{out} = B \leadsto \Gamma$ and $P_{in} = \Gamma \leadsto A$. Joining them at Γ and inserting e yields a subhamiltonian cycle.

It remains to show that we can always find two cycles that satisfy the requirements of Lemma 4. We neglect the degenerated case of Lemma 2, where $G_{out}(\mathcal{T})$ or $G_{in}(\mathcal{T})$ is a single vertex, because finding a cycle in that case is trivial. Consider for example $\overline{G}_{out}(\mathcal{T})$. To obtain $H_{out}(\mathcal{T})$, we temporarily replace \mathcal{T} in $\overline{G}_{out}(\mathcal{T})$ with a single vertex $v_{\mathcal{T}}$ as depicted in Fig. 3a. The resulting graph $G_{out}^*(\mathcal{T})$ remains 4-planar and triconnected, because $deg(v_{\mathcal{T}}) = 3$ by construction and any path via \mathcal{T} can use $v_{\mathcal{T}}$ instead. One may argue that this operation may introduce additional separating triangles. However, such a triangle must contain $v_{\mathcal{T}}$ and therefore, $deg(v_{\mathcal{T}}) = 4$, a contradiction. Now let us assume that $H_{out}^*(\mathcal{T})$ is a subhamiltonian cycle for $G_{out}^*(\mathcal{T})$. The idea is to reinsert \mathcal{T} and reroute $H_{out}^*(\mathcal{T})$ through \mathcal{T} such that the resulting cycle $H_{out}(\mathcal{T})$ contains two edges $e_1, e_2 \in E(\mathcal{T})$.

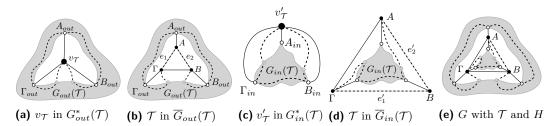


Figure 3 (a) Subhamiltonian cycle $H_{out}^*(\mathcal{T})$ in $G_{out}^*(\mathcal{T})$ containing $v_{\mathcal{T}}$. (b) Augmenting $H_{out}^*(\mathcal{T})$ yields $H_{out}(\mathcal{T})$ containing edges $e_1 = (\Gamma, A)$ and $e_2 = (A, B)$. (c) Dummy vertex $v'_{\mathcal{T}}$ as replacement for \mathcal{T} in $G_{in}^*(\mathcal{T})$ and a cycle $H_{in}^*(\mathcal{T})$. (d) Rerouting $H_{in}^*(\mathcal{T})$ through \mathcal{T} resulting in $H_{in}(\mathcal{T})$ with edges $e'_1 = (\Gamma, B)$ and $e_2 = (A, B)$. (d) The result of merging $H_{in}(\mathcal{T})$ and $H_{out}(\mathcal{T})$ into a cycle Hfor G.

Lemma 5. Let G be a triconnected 4-planar graph, \mathcal{T} a separating triangle. Furthermore, let $G_{out}^*(\mathcal{T})$ denote the graph resulting from replacing \mathcal{T} by a vertex $v_{\mathcal{T}}$ in $\overline{G}_{out}(\mathcal{T})$. A subhamiltonian cycle $H^*_{out}(\mathcal{T})$ for $G^*_{out}(\mathcal{T})$ can be augmented to a subhamiltonian cycle $H_{out}(\mathcal{T})$ for $\overline{G}_{out}(\mathcal{T})$ such that it contains two edges of \mathcal{T} , i.e., $E(H_{out}(\mathcal{T})) \cap E(\mathcal{T}) = \{e_1, e_2\}$. If $H_{out}^*(\mathcal{T})$ crosses every face of $G_{out}^*(\mathcal{T})$ at most once, one may choose any pair $e_1, e_2 \in E(\mathcal{T})$ to lie on $H_{out}(\mathcal{T})$.

Proof. We only sketch the proof. It is sufficient to consider every combination of e_1, e_2 and the location of the predecessor and successor of $v_{\mathcal{T}}$ in $H_{out}^*(\mathcal{T})$. It immediately becomes clear that in almost every situation $H^*_{out}(\mathcal{T})$ can be rerouted through \mathcal{T} such that the resulting cycle $H_{out}(\mathcal{T})$ contains two prescribed edges e_1, e_2 of \mathcal{T} . Only in one case, where $H_{out}^*(\mathcal{T})$ crosses an incident face twice to visit $v_{\mathcal{T}}$, a specific combination of edges is required.

In the single-separating triangle scenario, both $\overline{G}_{out}(\mathcal{T})$ and $\overline{G}_{in}(\mathcal{T})$ are free of separating triangles. Therefore, we may construct two graphs $G_{out}^*(\mathcal{T}), G_{in}^*(\mathcal{T})$ by replacing \mathcal{T} with dummy vertices. Applying Lemma 1 to them yields two subhamiltonian cycles $H_{out}^*(\mathcal{T})$ and $H_{in}^*(\mathcal{T})$, both crossing every face of $G_{out}^*(\mathcal{T})$ and $G_{in}^*(\mathcal{T})$ at most once. Hence, we may augment them with the aid of Lemma 5 such that they contain each two edges of \mathcal{T} . By choosing the combination of edges such that $H_{out}(\mathcal{T})$ and $H_{in}(\mathcal{T})$ meet the requirements of Lemma 4, we can merge them into a single subhamiltonian cycle ${\cal H}$ for ${\cal G}.$

While the property that $G_{out}^*(\mathcal{T})$ and $G_{in}^*(\mathcal{T})$ are both free of separating triangles enables us to conveniently choose two edges for each cycle $H_{out}(\mathcal{T}), H_{in}(\mathcal{T})$, this only works for a single separating triangle. However, a closer look reveals that it is sufficient to have a choice for either $H_{out}(\mathcal{T})$ or $H_{in}(\mathcal{T})$, not necessarily both of them. The idea is to first augment the cycle for which we do not have a choice to see which edges of \mathcal{T} are part of it, then we choose the edges for the second cycle accordingly. We summarize the idea as the main result of this section and describe it in a more formal manner in form of a proof.

▶ **Theorem 6.** Every triconnected 4-planar graph is subhamiltonian.

Proof. Let G denote a triconnected 4-planar graph and $\tau(G)$ the number of separating triangles in G. We prove by induction and claim that for any $\tau(G) \geq 0$, we can compute a subhamiltonian cycle H for G. Base case: Since $\tau(G) = 0$, we can directly apply Lemma 1. Inductive case: For $\tau(G) > 0$, we pick a separating triangle \mathcal{T} such that $\tau(\overline{G}_{in}(\mathcal{T})) = 0$. Let $G_{out}^*(\mathcal{T})$ be the result of replacing \mathcal{T} by $v_{\mathcal{T}}$ in $\overline{G}_{out}(\mathcal{T})$. Notice that $\tau(G_{out}^*(\mathcal{T})) = \tau(G) - 1$ holds. Hence, by induction hypothesis, $G_{out}^*(\mathcal{T})$ has a subhamiltonian cycle $H_{out}^*(\mathcal{T})$. We

reinsert \mathcal{T} and augment $H_{out}^*(\mathcal{T})$ such that the result $H_{out}(\mathcal{T})$ contains two (arbitrary) edges e_1, e_2 of \mathcal{T} . In a similar way, we replace \mathcal{T} in $\overline{G}_{in}(\mathcal{T})$ by $v_{\mathcal{T}}'$ to obtain $G_{in}^*(\mathcal{T})$. Since $\tau(\overline{G}_{in}(\mathcal{T})) = \tau(G_{in}^*(\mathcal{T})) = 0$ holds, we can apply Lemma 1 to $G_{in}^*(\mathcal{T})$ and compute a cycle $H_{in}^*(\mathcal{T})$ that crosses each face at most once. With Lemma 5 we may obtain a cycle $H_{in}(\mathcal{T})$ for $\overline{G}_{in}(\mathcal{T})$ with two edges $e_1', e_2' \in E(\mathcal{T})$ of our choice. Choosing $e_1' = e_1$ and $e_2' \neq e_2$ yields two cycles $H_{out}(\mathcal{T}), H_{in}(\mathcal{T})$ that meet the requirements of Lemma 4 and we can merge them into one cycle H for G.

The proof of Theorem 6 is constructive. Embedding G and identifying all separating triangles in G can be done in linear time. Augmenting a cycle and merging two of them takes constant time. Disjointness of separating triangles yields a linear number of subproblems and every edge occurs in at most one such subproblem. Hence, the total time spent for the subroutine of Lemma 1 is linear in the size of G.

▶ Corollary 7. A subhamiltonian cycle of a triconnected 4-planar graph can be found in linear time.

3 Two-Page Book Embeddings of General 4-Planar Graphs

In this section, we prove that any planar graph of maximum degree 4 admits a two-page book embedding. W.l.o.g. we assume that the input graph G is biconnected, since it is known that the page number of a graph equals the maximum of the page number of its biconnected components [2]. One can also neglect the exact geometry, as two edges that are drawn on the same page cross iff their endpoints alternate along the spine. We say that an edge e nests a vertex v iff one endpoint of e is to the left of v along the spine and the other endpoint of e to its right. We also say that an edge e nests an edge e' iff both e and e' are drawn on the same page and both endpoints of e' are nested by e. Observe that nested edges do not cross.

Our approach is as follows: First remove from G cycle C_{out} delimiting the outerface of G and contract each bridge-block of the remaining graph into a single vertex. Let F be the implied graph, which is a forest, as $G - C_{out}$ is not necessarily connected. C_{out} is embedded, s.t.: (i) the order of the vertices of C_{out} along the spine is fixed (and follows the one in which the vertices of C_{out} appear along C_{out}), and, (ii) all edges of C_{out} are on the same page, except for the one that connects its outermost vertices. Then we describe how to embed without crossings: (i) the chords of C_{out} , (ii) forest F, and, (iii) the edges between C_{out} and F. To obtain a two-page book embedding of G, we replace each vertex of F with a cycle (embedded similarly to C_{out}), whose length equals to the length of the cycle delimiting the outerface of the bridge-block it corresponds to in $G - C_{out}$, and recursively embed its interior.

More formally, consider an arbitrary simple cycle $C: v_1 \to v_2 \to \ldots \to v_k \to v_1$ of G. The removal of C results in two planar subgraphs $G_{in}(C)$ and $G_{out}(C)$ of G that are the components of G-C that lie in the interior and exterior of C in G, resp. Note that $G_{in}(C)$ and $G_{out}(C)$ are not necessarily connected. Let $\overline{G}_{in}(C)$ ($\overline{G}_{out}(C)$, resp.) be the subgraph of G induced by C and $G_{in}(C)$ ($G_{out}(C)$, resp.). For the recursive step, we assume the following invariant properties:

- IP-1: The order of the vertices of $\overline{G}_{out}(C)$ along the spine ℓ is fixed and the page in which each edge of $\overline{G}_{out}(C)$ is drawn (i.e., top- or bottom-drawn) is determined s.t. the book embedding of $\overline{G}_{out}(C)$ is planar. In other words, we assume that we have already produced a two-page book embedding for $\overline{G}_{out}(C)$, in which no edge crosses the spine.
- IP-2: The combinatorial embedding of $\overline{G}_{out}(C)$ is consistent with a given planar combinatorial embedding of G.

- IP-3: The vertices of C occupy consecutive positions along ℓ , s.t. v_1 (v_k , resp.) is the leftmost (rightmost, resp.) along ℓ . Moreover, all edges of C are on the same page, except for the one that connects v_1 and v_k . Say w.l.o.g. that (v_1, v_k) is on the top-page (or top-drawn), while the remaining edges of C, namely edges (v_i, v_{i+1}) for $1 \le i < k$, are on the bottom-page (or bottom-drawn).
- IP-4: If C is not identified with the cycle delimiting the outerface of G, the degree of either v_1 or v_k is at most 3 in $\overline{G}_{in}(C)$. Say w.l.o.g. that v_k is of degree at most 3.
- IP-5: If vertex v_1 has degree 4 in $\overline{G}_{in}(C)$, then it is adjacent to zero or two chords of C.

We note that the combinatorial embedding specified in IP-2 is maintained throughout the whole drawing process. This combined with the fact that every edge entirely lies on one page is sufficient to ensure planarity. Note that we first present the recursive step of our algorithm and then its base, as this approach shows better how the different ideas flow one after the other. Let v_i be a vertex of C, $i=1,\ldots,k$. Since G is of max-degree 4, v_i is incident to at most two undrawn edges. Assume that v_i has at least one undrawn edge. We refer to the edge incident to v_i that follows $(v_i,v_{(i+1) \mod k})$ in the counterclockwise order of the edges around v_i (as defined by the combinatorial embedding specified by IP-2), as the right edge of v_i . If v_i is adjacent to two undrawn edges, then the one that is not identified with the right edge of v_i is its left edge; otherwise, the left and the right edge of v_i are identified.

Initially, we draw the chords of C on the top-page. By IP-2 and IP-3, no two chords intersect. We then draw $G_{in}(C)$ and the edges between C and $G_{in}(C)$. Note that $G_{in}(C)$ is not necessarily connected. Hence, its bridge-block trees form a forest. As already stated, we contract each bridge-block of $G_{in}(C)$ into a single vertex, which we call block-vertex; see Figs. 4a-4b. We distinguish two types of block-vertices: those adjacent to vertices of C(anchors) and those adjacent to other block-vertices only (ancillaries). From the contraction, it follows that an edge between C and a certain anchor can be of multiplicity at most two. Edges among block-vertices are always simple. We will first determine the positions of all anchors along ℓ . Consider an anchor c, then among the edges between c and C, we select and mark exactly one, s.t.: (i) the marked edge will be drawn on the bottom-page and (ii) all other edges incident to c (i.e., either edges between c and C that are not marked, or between c and block-vertices) will be drawn on the top-page. Let $v_{l,c}$ be the leftmost vertex of C adjacent to c along ℓ . If $(c, v_{l,c})$ is simple, we select and mark this edge. Otherwise, we mark the right edge of $v_{l,c}$. Hence, each anchor has exactly one marked edge and each vertex of C is incident to at most two marked edges. Let $v \in C$ be a vertex of C adjacent to at least one anchor through a marked edge. We distinguish two cases:

- Case 1 v is adjacent to exactly two anchors c and c' through two marked edges e and e', resp.: Assume w.l.o.g. that e is the left edge of v and e' its right edge. Then, both c and c' are placed directly to the right of v and c precedes c' (see Fig. 4d). Note that v cannot be the rightmost vertex of C due to IP-4.
- Case 2 v is adjacent to one anchor c through a marked edge e: If deg(v) = 3 in $\overline{G}_{in}(C)$, then we distinguish two sub-cases. If v is not the rightmost vertex of C, then c is placed directly to the right of v (see Fig. 4e). Otherwise, directly to its left (see Fig. 4f). It now remains to consider the case where deg(v) = 4 in $\overline{G}_{in}(C)$. In this case, by IP-4 it follows that v is not the rightmost vertex of C. Again, we distinguish two sub-cases:
 - If e is the right edge of v, then c is placed directly to the right of v (see Fig. 4g).
 - If e is the left edge of v, then c is placed directly to the left of v (see Fig. 4h); v cannot be the leftmost vertex of C, as the right edge of v would be a chord, violating IP-5.

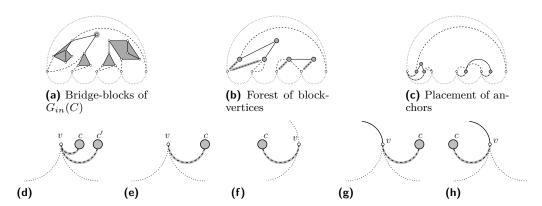


Figure 4 In all figures, the edges of C are drawn dotted, bridge-blocks are colored gray and edges between C and anchors are drawn dashed; marked edges are highlighted in gray.

All marked edges are bottom-drawn. Edges between anchors and C that are not marked are top-drawn; see Fig. 4c. Observe that we do not change the underlying combinatorial embedding of G, preserving IP-2. Hence, the book embedding constructed so far is planar.

Now observe that ancillaries form a new forest (forest of ancillaries), which is a subgraph of the initial forest containing all block-vertices. Let T be a tree of the forest of ancillaries and let c_1, \ldots, c_t be anchors that (i) are adjacent to at least one ancillary of T, and (ii) c_i is to the left of c_{i+1} , $i = 1, \ldots, t-1$. We refer to c_1, \ldots, c_t as the anchors of T, and to the tree formed by T and its anchors as the anchored tree of T, denoted by \overline{T} . We say that two anchors of \overline{T} are consecutive iff there is no anchor of \overline{T} between them.

▶ Lemma 8. For anchored trees the following hold: (i) Two trees \overline{T} and $\overline{T'}$ share at most a common anchor; (ii) \overline{T} contains at least two anchors; and (iii) every leaf of \overline{T} is an anchor of \overline{T} , and vice versa.

Proof. The assumption that one of the two properties does not hold contradicts either the connectivity or biconnectivity of G.

Assume now that \overline{T} is rooted at anchor c_1 (rooted anchored tree). For an anchor or ancillary c of \overline{T} , denote by p(c) the parent of c in \overline{T} and let $p(c_1)$ be any of the vertices of C adjacent to c_1 . For an ancillary c of T (i.e., non-leaf in \overline{T}), we define an order for its children: if c' and c'' are children of c, then c' < c'' iff c' precedes c'' in the counterclockwise order of the edges around c (defined by the combinatorial embedding specified by IP-2), when starting from (c, p(c)). By this order, we label the vertices of \overline{T} as they appear in the pre-order traversal of \overline{T} (labeled anchored tree); see Fig.5a.

▶ **Lemma 9.** For each ancillary c of a labeled anchored tree \overline{T} there is (i) at least an anchor of \overline{T} with label smaller than that of c and (ii) at least another with label greater than that of c

Proof. The leftmost anchor (i.e. root) of \overline{T} is zero labeled, which proves (i). The greatest labeled vertex of \overline{T} is a leaf of \overline{T} (due to pre-order traversal) and by Lemma 8(iii) an anchor of \overline{T} which proves (ii).

We first define the order in which the trees of the forest of ancillaries will be drawn. Let G_{aux}^T be an auxiliary graph whose vertices correspond to trees and there is a directed edge $(v_{T'}, v_T)$ in G_{aux}^T iff $\overline{T'}$ has an anchor between two consecutive anchors of \overline{T} . The desired order is defined by a topological sorting of G_{aux}^T , which exists due to the following lemma.

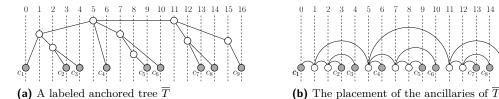


Figure 5 In both figures, anchors are colored gray; the indices of the vertical grid-lines denote the labeling of \overline{T} .

▶ Lemma 10. Auxiliary graph G_{aux}^T is a directed acyclic graph.

Proof. Assume to the contrary that there is a cycle $v_{T_1} \to \dots v_{T_s} \to v_{T_1}$ in G^T_{aux} . Let I_i be the interval defined by the left/right-most anchors of $\overline{T_i}$. Edge $(v_{T_i}, v_{T_{i+1}})$ implies that there is an anchor of $\overline{T_i}$ between consecutive anchors of $\overline{T_{i+1}}$. However, in this case all anchors of $\overline{T_i}$ should be between the same two anchors of $\overline{T_{i+1}}$, as otherwise the embedding specified by IP-2 is not planar. So, $I_i \subseteq I_{i+1}$. By Lemma 8(i), it follows that $I_i \neq I_{i+1}$. Hence, $I_1 \subset \dots \subset I_s \subset I_1$, a contradiction.

Lemma 10 implies that drawing the trees in the order defined by a topological sorting of G_{aux}^T , assures that the tree T' will be drawn before T, if $\overline{T'}$ has an anchor that is between two consecutive anchors of \overline{T} along ℓ . Now assume that we have drawn zero or more of these trees s.t. (i) all edges are top-drawn, (ii) there are no edge crossings, and (iii) the combinatorial embedding specified by IP-2 is preserved. Let T be the next tree to be drawn. The following lemma presents an important property of our drawing approach.

▶ Lemma 11. Assume that all trees that precede T in a topological sorting of G_{aux}^T have been drawn on the top-page without edge crossings by preserving the combinatorial embedding specified by IP-2. If e is a top-drawn edge that does not belong to \overline{T} and nests at least one anchor of \overline{T} , then it nests all anchors of \overline{T} .

Proof. The detailed proof is based on a case-analysis on the type of edge e: (i) top-drawn edge of C; (ii) edge of an anchored tree $\overline{T'}$ drawn before \overline{T} ; and (iii) not an edge of a previously drawn anchored tree (i.e., each endpoint of e is either a vertex of C or an anchor).

We now describe how to draw T on the top page s.t. (i) there are no edge crossings, and, (ii) the combinatorial embedding specified by IP-2 is preserved. More precisely, we place each ancillary c of T between a pair of consecutive anchors of \overline{T} , s.t. the label of c is larger (smaller) than the label of the anchor to its left (right)¹; for ancillaries placed between the same pair of anchors, the one with smaller label is to the left; all edges of \overline{T} are top-drawn (see Fig.5b). Note that we have not fully specified the exact positions of the ancillaries of \overline{T} along ℓ , since between consecutive anchors of \overline{T} there may exist anchors that do not belong to \overline{T} or vertices of C or anchors/ancillaries of trees that have already been drawn. Details will be given shortly. Notice that all ancillaries of \overline{T} are placed between its left/right-most anchors, which by Lemma 11 implies that if a top-drawn edge (that does not belong to \overline{T}) nests at least one anchor of \overline{T} , then it nests the entire tree \overline{T} . By exploiting the correspondence between the left-to-right order of the vertices of \overline{T} along ℓ and the labeling of \overline{T} , we can prove that the drawing of \overline{T} is planar.

Note that the existence of this pair of consecutive anchors of \overline{T} is implied by Lemma 9.

▶ **Lemma 12.** The drawing of the anchored tree \overline{T} is planar.

Proof. Assume to the contrary that $e = (c_1, c_2)$ and $e' = (c'_1, c'_2)$ of \overline{T} cross. Since e and e' are top-drawn, their endpoints alternate along ℓ . Let the order on ℓ be $c_1 \to c'_1 \to c_2 \to c'_2$. Hence, c_1 is the parent of c_2 , as the label of c_1 is smaller than that of c_2 and they are adjacent in \overline{T} . Similarly, c'_1 is the parent of c'_2 . Since between c_1 and c_2 are drawn subtrees of \overline{T} rooted at children of c_1 other than c_2 , c'_1 and c'_2 belong to a subtree rooted at a child of c_1 , different from c_2 , which implies that the label of c'_2 is smaller than that of c_2 , a contradiction.

Recall that we have not fully specified the exact positions of the ancillaries of \overline{T} along ℓ . Consider the following scenario. There is a path P of top-drawn edges (e.g., non-marked edges incident to C and/or edges of previously drawn trees) joining a pair of consecutive anchors of \overline{T} and our algorithm must place an ancillary c of \overline{T} between them. Since c is nested by an edge of P and all edges of \overline{T} are top-drawn, an edge connecting c with an ancillary of \overline{T} placed between another pair of consecutive anchors of \overline{T} will cross P. The following lemma ensures that this scenario cannot occur, as such a path cannot exist.

▶ Lemma 13. Let $u_0, u_1, \ldots, u_{l+1}, l \geq 0$, be vertices (anchors/ancillaries are treated as vertices) drawn on ℓ from left to right, s.t. u_0 and u_{l+1} are two consecutive anchors of \overline{T} . Assume that all trees anchored at u_1, \ldots, u_l have been drawn on the top-page without edge crossings by preserving the combinatorial embedding specified by IP-2, while T has not been drawn. Then, there is an index $i \in \{0, 1, \ldots, l\}$, such that no two adjacent vertices u_k and u_m exist with $0 \leq k \leq i$, $i + 1 \leq m \leq l + 1$ and (u_k, u_m) is top-drawn.

Proof. Assume to the contrary that for all $i \in \{0, ..., l\}$, there are two adjacent vertices u_k and u_m with $0 \le k \le i$, $i+1 \le m \le l+1$ and (u_k, u_m) is on the top-page. One can prove that there is a top-drawn path $P(u_0 \to u_{l+1}): u_0 \to u_{j_1} \dots u_{j_p} \to u_{l+1}$ consisting of vertices of $\{u_0, \ldots, u_{l+1}\}$, whose edges are top-drawn and for each edge of $P(u_0 \to u_{l+1})$ there is not a top-drawn edge with endpoints in $\{u_0, \ldots, u_{l+1}\}$ that nests it. However, the existence of $P(u_0 \to u_{l+1})$ implies that G should contain a vertex of degree five, a contradiction.

We are now ready to specify the exact positions of the ancillaries of \overline{T} along ℓ . Assume that a particular number of ancillaries of \overline{T} should be drawn between two consecutive anchors c_i and c_{i+1} of \overline{T} , $i=1,\ldots,t-1$. By Lemma 13, there is a pair of vertices that are between c_i and c_{i+1} along ℓ and there is not a top-drawn edge with endpoints between c_i and c_{i+1} nesting both of these vertices. We place between this pair of vertices all ancillaries of \overline{T} that must reside between c_i and c_{i+1} , without changing their relative order, i.e., for ancillaries placed between c_i and c_{i+1} , the one with smaller label is to the left. Lemma 12 ensures the planarity of \overline{T} . It remains to prove that the combinatorial embedding specified by IP-2 is preserved.

▶ **Lemma 14.** Assume that all trees that precede T in a topological sorting of G_{aux}^T have been drawn on the top-page without edge crossings by preserving the combinatorial embedding specified by IP-2. When \overline{T} is drawn, the combinatorial embedding specified by IP-2 is also preserved.

Proof. We only sketch the proof. Since the drawing of \overline{T} preserves the order of the edges around all ancillaries, the combinatorial embedding specified by IP-2 is preserved for all ancillaries of \overline{T} . Then, one can prove that this property is propagated to all vertices of T.

The following lemma focuses on the case where C contains a vertex with degree 2 in $\overline{G}_{in}(C)$ (other than its leftmost or rightmost vertex). We will utilize this lemma later.

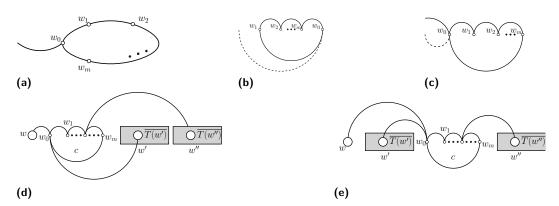


Figure 6 (a) The outerface of a block-vertex c. (b)–(c) different cases that occur when drawing the outerface of c, in the case where c is anchor. (d) Ancillary c needs to be repositioned. (e) Its placement is determined by Lemma 16.

▶ **Lemma 15.** Let v be a vertex of C with degree 2 in $\overline{G}_{in}(C)$ that is not the left/right-most vertex of C. Let also v_r (v_l) be its next neighbor on C to its right (left resp.). Since edge (v, v_r) belongs to C, it is drawn on the bottom-page. However, it can also be drawn on the top-page without edge-crossings, while the combinatorial embedding specified by IP-2 is maintained.

Proof. We only sketch the proof. First observe that if no block-vertex is drawn between vand v_r , then obviously (v, v_r) can be drawn on the top-page. Otherwise, one can move the block-vertices in between to the left of v, so that v and v_r are consecutive along ℓ .

Up to now, we have drawn $\overline{G}_{in}(C)$, s.t., every bridge-block of $G_{in}(C)$ is contracted to a block-vertex that lies on ℓ and each edge is drawn either on the bottom (if it is a marked edge) or on the top-page (otherwise). Next, we describe how to recursively proceed. Let cbe a block-vertex of $G_{in}(C)$ with outerface \mathcal{F}_c . Initially, assume that \mathcal{F}_c is a simple cycle. If c is an anchor, denote by w_0 the vertex of \mathcal{F}_c incident to the marked edge of c. If c is an ancillary, then c belongs to an anchored tree. In this case, w_0 denotes the vertex of \mathcal{F}_c adjacent to the closest neighbor of c to its left, which is well-defined since c is always placed between two consecutive anchors of the anchored tree it belongs to. Let w_0, w_1, \ldots, w_m be the vertices of \mathcal{F}_c , in the clockwise traversal of \mathcal{F}_c from w_0 (see Fig. 6a).

If c is an anchor (i.e., w_0 is incident to a marked edge), then we place the vertices of \mathcal{F}_c on ℓ as follows: (i) w_0 occupies the position of c and it is the rightmost vertex of \mathcal{F}_c on ℓ , (ii) w_1 is the leftmost vertex of \mathcal{F}_c on ℓ , (iii) w_i is to the left of w_{i+1} for $i=1,\ldots,m-1$, and, (iv) there are no vertices in between; see Fig. 6b. All edges of \mathcal{F}_c are top-drawn, except for (w_1, w_0) . This placement is feasible, except for the case in which in the combinatorial embedding specified by IP-2 there is an edge incident to w_0 that is between (w_0, w_1) and the marked edge incident to w_0 in the counterclockwise order of the edges around w_0 when starting from (w_0, w_1) ; see Fig. 6c. In this case, we place w_0 to the left of w_1, \ldots, w_m , s.t. w_0 is the leftmost vertex of \mathcal{F}_c . So, (w_0, w_m) is the bottom-drawn edge of \mathcal{F}_c .

Suppose now that c is an ancillary. Let w be the closest neighbor of c to its left on ℓ . w is the parent of c in the tree in which c belongs to and (w_0, w) is top-drawn. We place the vertices of \mathcal{F}_c as follows: (i) w_0 occupies the position of c and it is the leftmost vertex of \mathcal{F}_c on ℓ , (ii) w_m is the rightmost vertex of \mathcal{F}_c on ℓ , (iii) w_i is to the left of w_{i+1} , $i=1,\ldots,m-1$, and, (iv) there are no vertices in between. All edges of \mathcal{F}_c are top-drawn, except for (w_0, w_m) . This placement is infeasible only when in the combinatorial embedding specified by IP-2 there

is an edge incident to w_0 , say (w_0, w') , and between (w_0, w_m) and (w_0, w) in the clockwise order of the edges around w_0 when starting from (w_0, w_m) ; see Fig. 6d. Since c has only its parent to its left among the block-vertices of the anchored tree it belongs to, it follows that, w' is to the right of c. So, (w_0, w') cannot be drawn on the top-page, without deviating the combinatorial embedding specified by IP-2. Since G is biconnected, c is adjacent to at least another block-vertex, say w'', s.t. $w'' \notin \{w, w'\}$. The following lemma takes care of this case.

▶ **Lemma 16.** Ancillary c can be repositioned on ℓ , s.t.: (i) c is placed between two consecutive anchors of \overline{T} . (ii) The combinatorial embedding specified by IP-2 is preserved and the edges (w_0, w) , (w_0, w') and (c, w'') are top-drawn and crossing-free. (iii) w_0 is leftmost vertex of \mathcal{F}_c and w_i is to the left of w_{i+1} , $i = 1, \ldots, m-1$; All edges of \mathcal{F}_c are top-drawn, except for (w_0, w_m) .

Proof. w is the parent of c and w', w'' are children of c in \overline{T} , with w' being the first child of c. For our proof, w'' is its second child. So, (c, w), (c, w') and (c, w'') are consecutive around c as in Fig. 6d. Let $\overline{T(w')}$ and $\overline{T(w'')}$ be subtrees of \overline{T} rooted at w' and w'', resp. c is to the left of all vertices of $\overline{T(w')}$, all vertices of $\overline{T(w')}$ are to the left of all vertices of $\overline{T(w'')}$ and there are no ancillaries of \overline{T} in between. We place c between the rightmost (leftmost) anchor of $\overline{T(w')}$ ($\overline{T(w'')}$); see Fig. 6e. So, c is placed between two consecutive anchors of \overline{T} . If we place the vertices of \mathcal{F}_c , with w_0 being leftmost on \mathcal{F}_c and w_i to the left of w_{i+1} , then (w_0, w) , (w_0, w') and (c, w'') are drawn on the top-page and the embedding is preserved.

If we process all ancillaries that have to be repositioned from right to left along ℓ , then by Lemma 16 we obtain a planar drawing in which the embedding specified by IP-2 is preserved

once the outerface of each block-vertex is drawn and all edges that connect block-vertices are eventually drawn on the toppage. Initially, we assumed that \mathcal{F}_c is simple. If not so, \mathcal{F}_c consists of smaller simple *subcycles*, s.t. (i) any two subcycles share at most one vertex of \mathcal{F}_c and (ii) any vertex of \mathcal{F}_c is incident to at most two subcycles. Hence, the "tangency graph" of these subcycles (which has a vertex for each subcycle and an edge between every pair of subcycles that share a vertex) is a tree. Define w_0 as in the case of simple cycle and let the

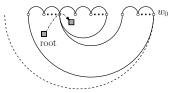


Figure 7 \mathcal{F}_c is not simple.

tangency tree be rooted at the cycle containing w_0 . Due to degree restriction, w_0 cannot be incident to two subcycles. We draw the subcycles of \mathcal{F}_c in the order implied by the Breadth First Search (BFS) traversal of the tangency tree. The first one (incident to w_0) is drawn as in the case of simple cycle. Each next subcycle is plugged into the drawing, as in Fig. 7.

It remains to ensure that IP-1 up to IP-5 are satisfied when a simple cycle, say C_s , is recursively drawn. IP-1 holds, since each edge is drawn either on the bottom (if it is a marked edge) or on the top-page (otherwise) and no two edges intersect. Lemma 14 implies IP-2. If C_s is the outerface of a block-vertex or a leaf in the tangency tree, then IP-3 trivially holds. If C_s is a non-leaf in the tangency tree, it contains at least one edge on the bottom-page (see Fig. 7). This violates IP-3. However, we can benefit from Lemma 15 since the edge which is improperly bottom-drawn is incident to a vertex (of degree four) that is not adjacent to any other vertex in the interior of C_s . For the sake of the recursion we assume that it is drawn on the top-page and once C_s is completely drawn, we redraw it on the bottom-page using Lemma 15. If C_s is the outerface of a block-vertex or root of the tangency tree of a non-simple outerface \mathcal{F}_c , then at least one vertex of C_s is adjacent to $G_{out}(C_s)$. If C_s is an internal node of the tangency tree of \mathcal{F}_c , then its leftmost vertex has two edges in $G_{out}(C_s)$. Hence, IP-4 also holds. Note that IP-5 does not necessarily hold. However, we can identify a

maximal separating path of chords of C_s adjacent to its leftmost vertex and use it to create two subinstances, which can be recursively drawn; refer to [1] for details.

The recursion begins by specifying a drawing of G with a chordless outerface $C_{out}: v_1 \to \dots v_k \to v_1$. We place v_1, \dots, v_k in this order along ℓ and draw the edges of C_{out} as imposed by IP-3. If there is a vertex of C_{out} with degree less than four, then it is chosen as v_k and all invariant properties are satisfied. Otherwise, we appropriately augment our graph, so that IP-4 holds (the detailed proof is given in [1]). We are now ready to state our main theorem.

▶ **Theorem 17.** Any planar graph of maximum degree 4 on n vertices admits a two-page book embedding, which can be constructed in $O(n^2)$ time.

Proof. At each step, our algorithm performs a series of computations; the computation of the bridge-blocks, the topological sorting of G_{aux}^T , BFS-traversals on the tangency trees. All of these computations can be done in O(n) time, resulting in $O(n^2)$ total time.

4 Conclusions and Open Problems

Two approaches were proposed to embed a 4-planar graph into two pages. One reasonable question arising at this point is whether the result can be extended to 5-planar graphs.

- References

- Michael A. Bekos, Martin Gronemann, and Chrysanthi N. Raftopoulou. Two-page book embeddings of 4-planar graphs. Arxiv report arxiv.org/abs/1401.0684, 2013.
- 2 Frank Bernhart and Paul C. Kainen. The book thickness of a graph. *Journal of Combinatorial Theory*, 27(3):320–331, 1979.
- 3 Chiuyuan Chen. Any maximal planar graph with only one separating triangle is hamiltonian. *Journal of Combinatorial Optimization*, 7(1):79–86, 2003.
- 4 Norishige Chiba and Takao Nishizeki. The hamiltonian cycle problem is linear-time solvable for 4-connected planar graphs. *Journal of Algorithms*, 10(2):187–211, 1989.
- 5 Fan R. K. Chung, Frank T. Leighton, and Arnold L. Rosenberg. Embedding graphs in books: A layout problem with applications to VLSI design. *SIAM Journal on Algebraic and Discrete Methods*, 8(1):33–58, 1987.
- **6** Vida Dujmovic and David R. Wood. On linear layouts of graphs. *Discrete Mathematics & Theoretical Computer Science*, 6(2):339−358, 2004.
- 7 Lenwood S. Heath. Algorithms for Embedding Graphs in Books. PhD thesis, University of North Carolina, Chapel Hill, 1985.
- **8** Guido Helden. Each maximal planar graph with exactly two separating triangles is hamiltonian. *Discrete Applied Mathematics*, 155(14):1833–1836, 2007.
- **9** Paul C. Kainen and Shannon Overbay. Extension of a theorem of Whitney. *Applied Mathematics Letters*, 20(7):835–837, 2007.
- 10 Daniel P. Sanders. On paths in planar graphs. *Journal of Graph Theory*, 24(4):341–345, 1997.
- William T. Tutte. A theorem on planar graphs. Transactions of the American Mathematical Society, 82(1):99–116, 1956.
- 12 Hassler Whitney. A theorem on graphs. Annals of Mathematics, 32(2):378–390, 1931.
- 13 Avi Wigderson. The complexity of the hamiltonian circuit problem for maximal planar graphs. Technical Report TR-298, EECS Department, Princeton University, 1982.
- 14 Mihalis Yannakakis. Embedding planar graphs in four pages. *Journal of Computer and System Sciences*, 38(1):36–67, 1989.