An Increasing Need for Formality

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— Abstract –

The talk will touch on a number of practical opportunities for formal modeling and methods that Intel sees in HW security research including: instruction sets; the proliferation of programmable agents within SoC's; and negative space testing.

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 $\textbf{Keywords and phrases} \ \ \text{Hardware security, formal modeling, instruction sets, SoC's, negative space testing}$

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 ${\color{red}\textbf{Category}} \ \, \textbf{Invited} \, \, \textbf{Talk}$