# **Constraint Programming Model for Assembly Line Balancing and Scheduling with Walking Workers and Parallel Stations**

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## **Abstract**

In the context of aircraft assembly lines, increasing the production rate and decreasing the operating costs are two important, and sometimes contradictory, objectives. In small assembly lines, sharing production resources across workstations is a simple and efficient way to reduce operating costs. Therefore, workers are not assigned to a unique workstation but can walk between them. On the other side, paralleling workstations is an efficient way to increase the production rate. However, the combination of both strategies create complex conditions for tasks to access the production resources. This paper addresses the problem of allocating tasks to workstations and scheduling them in an assembly line where workers can freely walk across workstations, and where workstations can be organized in parallel. We model this novel problem with Constraint Programming. We evaluate it on real world industrial use cases coming from aircraft manufacturers, as well as synthetic use cases adapted from the literature.

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# **1 Introduction**

The production of complex artefacts such as aircraft is commonly performed by the use of assembly lines. In an assembly line, several workstations are dedicated to a subset of assembly tasks, and the artefacts being built travel from one workstation to another until they are complete. As each workstation is always occupied by one instance of the artefact, all instances visit the workstations in the same order, and travel at the same time, after a fixed time interval called the *cycle time*. Since the cycle time is the duration between the completion of two instances of the artefact, its value is of critical importance for business purposes, and a lot of research aims at improving (i.e. minimizing) its value for all sorts of production systems.

A natural way to improve the production rate, without modifying the artifact design, is simply to add more workstations. Since the same amount of work is divided in a larger number of workstations, each workstation has less tasks and complete them faster, thus

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decreasing the cycle time. The main drawback of adding workstations is that it occupies more space in the factory and may not always be possible. Furthermore, even when it is possible, the cost of creating a new workstation can be another obstacle. While in large assembly lines each workstation has its dedicated equipment and operators, this is not necessarily the case in smaller assembly lines, where it is possible for operators to travel, and equipment to be transferred, across workstations. In this context, adding a new workstation can be much cheaper if expensive equipment does not need to be duplicated.

Another way to increase the production rate is to act on the artefact flow, and in particular to extend a production stage with parallel workstations. The requirement of having each task confined to a single workstation then translates to weaker temporal constraints, and it even allows the cycle time to be smaller than the duration of some tasks. It also has the drawback of occupying more space in the factory since more routes must be cleared for the artifacts to travel. Moreover, parallel workstations with shared resources can create complex situations in terms of conflicting access by tasks to resources. These conditions are precisely the highlight of this paper.

In this paper, inspired by an industrial assembly line for small aircraft, we address the problem of increasing the production rate, and use it to evaluate different assembly line structures (number of stages, number of parallel workstations), with a predefined amount of resources. More precisely, our assembly line model has the following features:

- Minimizing the assembly duration.  $\sim$
- The number and layout of workstations is an input of the problem. The layout specifies the number of serial stages, and the number of parallel duplicate workstations in each stage.
- $\blacksquare$  Each workstation has different zones (e.g. inside or outside of the aircraft, front or rear, etc.), and each task occupies one or more zones. Two tasks that occupy the same zone cannot be performed simultaneously.
- Workers can walk freely across workstations. The travel time between workstations is not accounted for, as it is considered negligible with respect to the work time.
- Each worker has one specific skill, and each task requires a given number of workers with each skill (including zero). At each instant a worker can only perform one task, which means tasks compete with one another for accessing workers. As we have walking workers, this competition occurs not only inside workstations, but also across workstations.
- $\blacksquare$  As parallel workstations allow for tasks longer than the cycle time, such a long task can compete with itself for accessing workers. More precisely, two instances of the task performed on two parallel workstations, but shifted by only one cycle, overlap, and thus require twice the amount of workers. This aspect is original, and is explained and illustrated in detail.
- $\blacksquare$  In the problem output, in addition to the smallest possible cycle time, each assembly task is assigned to a unique workstation (or rather a stage in the case of parallel workstations), and is given start and end dates. Moreover, tasks are not preemptive.

We can summarize the contribution of this work as follows:

- we address a novel extension of the Assembly Line Balancing (ALB) problem in which we schedule all tasks, some resources can walk between stations and stations can be parallel;
- we propose a Constraint Programming (CP) model for this problem ;
- we present results associated with data coming from an aircraft manufacturer, which show the applicability of the approach;

 $\blacksquare$  we experiment on public benchmarks that we have adapted to our hypotheses: some corresponding to aircraft assembly lines and the others based on public Resource-Constrained Project Scheduling Problem (RCPSP) instances.

This paper is organized as follows. First the state of the art is reviewed in Section [2,](#page-2-0) around the ALB and RCPSP variants, and constraint programming in the aeronautics industry. Second, a more detailed account of the assembly line is provided in Section [3,](#page-4-0) and in particular how parallel workstations behave, and how it impacts the competition for resources between tasks. The CP model is then described in Section [4](#page-6-0) along with an illustrative example. Finally, Section [5](#page-12-0) relates our experiments on both academic benchmarks and industrial examples. We describe perspectives of this work in Section [6.](#page-14-0)

# <span id="page-2-0"></span>**2 Related Work**

The work presented in this paper can be compared with two well known optimisation problems: the Assembly Line Balancing (ALB) problem and the Resource-Constrained Project Scheduling Problem (RCPSP). ALB essentially deals with assigning tasks to workstations [\[11\]](#page-15-0). In particular, the Simple Assembly Line Balancing Problem version 2 (SALB2) aims at minimizing the cycle time given a list of tasks, a list of workstations and precedence constraints between tasks. However, in a classical ALB problem, tasks are only assigned to workstations, but are not scheduled, i.e. they are not assigned a start and end date.

A family of variants of ALB addresses assembly lines where some workstations perform the same tasks in parallel. More precisely, the assembly line is composed of a series of *stages*, each stage contains one or several parallel workstations. Each product goes through each stage in order, but only on one workstations at each stage. Using parallel workstations helps decreasing the cycle time, but requires more resources. Parallel ALB approaches minimize either the cycle time or total cost associated to workstations, and assign tasks to workstations, but do not schedule them [\[12,](#page-16-0) [3,](#page-15-1) [4,](#page-15-2) [16\]](#page-16-1).

In a classical RCPSP, given a list of tasks, precedence constraints between tasks, a list of resources and a consumption relation between tasks and resources, the problem is to assign a start date and an end date to each task so as to minimize the total work time [\[18\]](#page-16-2). The main difference with ALB is that the work is not divided among synchronized workstations. However, by reasoning on start and end dates, it supports a precise management of resources. Our approach can be seen as an extension of RCPSP with workstations, work zones in each workstation, and cumulative resources shared across workstations. This is how we could evaluate our approach on the PSPlib benchmark [\[19\]](#page-16-3).

The RCPSP enjoys a vast amount of variants [\[15\]](#page-16-4). Among them is the Multi-Skill RCPSP, in which workers have one or several skills that are required for executing tasks. Our approach does not feature this aspect. Each worker has a unique skill, and a set of *n* workers with a particular skill can be directly modeled as a renewable resource with a capacity equal to *n*.

There are many extensions of ALB that incorporate RCPSP aspects. Resource Constrained ALB (RCALB) accounts for the resources deployed on each workstation and for how tasks use resources [\[2,](#page-15-3) [13,](#page-16-5) [7\]](#page-15-4), much like RCPSP cumulative resources, or [\[1\]](#page-15-5) that supports multi-mode RCPSP constraints. In RCALB, resources are assigned to a single workstation, which differs from our approach where resources are shared across workstations.

The variant of Multi-Manned ALB resembles RCALB, except in the case of Walking Workers. These approaches have many common points with ours and are detailed in a dedicated section [2.1.](#page-3-0) ALB and RCPSP have been combined in other ways described in [\[8\]](#page-15-6).

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Multi-Model ALB models assembly lines where several types of product are manufactured. In this variant, many approaches not only assign tasks to workstations but also schedule them. [\[24\]](#page-16-6) has a list of tasks with precedence constraints in order to assemble a set of products. Tasks are scheduled on workstations accounting for precedence, equipment, surface and buffers, but completely ignores possible future tasks. In [\[25\]](#page-16-7) the problem is modified to consider cyclic schedules, in which the list of tasks is expected to be repeated, and the concept of cycle time appears again, event though the assembly line is not pulsated. [\[21\]](#page-16-8) also computes cyclic schedules for multi-model assembly lines, however the products advance on a treadmill and workstations are not separated and organized into stages.

Some variants of the ALB problem support walking workers. In [\[32\]](#page-17-1) workers can move from one station to another, and the time to travel between stations is accounted for under a form similar to the Traveling Salesman Problem. However, tasks are not scheduled inside each station. [\[10\]](#page-15-7) addresses mixed model assembly lines and assigns workers to workstations. However, the assignment of tasks to workstations is predefined as input.

## <span id="page-3-0"></span>**2.1 Multi-manned ALB with Walking Workers**

An important family of variants of ALBP concerns Multi-manned ALB (MMALBP). Some of these variants model the workers needed to perform tasks in a way similar to how tasks use resources in a RCPSP, much like this paper does. As a consequence, many approaches in this family not only assign tasks to workstations, but also schedule their start and end dates in order to ensure that the tasks assigned to each worker are feasible, both in volume and precedence. In addition, some of these variants have walking workers that travel across workstations. Many of these variants bear significant similarities with our work.

[\[14\]](#page-16-9) proposes Mixed Integer Linear Programming (MILP) models for MMALB. Each worker is assigned to a station, and each task is assigned to both a workstation and a worker, and is scheduled inside its station. Workers cannot travel to another workstation, and working zones and parallel workstations are not modelled.

[\[30\]](#page-17-2) addresses MMALBP with walking workers. Each worker can travel to a predefined set of stations (possibly all), and each station can host several workers at once. Each task is assigned to a station and a worker, and is scheduled inside its station. It only differs with this work in that working zones, and more importantly parallel workstations, are not accounted for.

[\[23\]](#page-16-10) describes MMALB with moving workers. Each task has a set of compatible workers, which models worker skills, and a set of mutually exclusive tasks, which could be used to model working zones. Each workstation is divided into sides, and each worker is assigned to at most two sides of a single workstation. Each task requires one or several sides, and is assigned to a single station, as well as one worker per required side. Tasks are not scheduled by date, but a partial ordering is computed during optimization. Workers only travel across sides of a single workstation; furthermore parallel workstations are not modelled in this paper.

[\[22\]](#page-16-11) addresses MMALB in production lines where the product does not advance in pulses from one station to another, but rather advances continuously as on a treadmill. Tasks are assigned to workers and scheduled inside an envelope called "cycle time" so as to occupy a bounded region of the treadmill. However, there are no global date boundaries like in pulsated assembly lines.

[\[9\]](#page-15-8) defines variable workplaces ALB, in which each workstation has a list of so-called "mounting positions", that are gathered into workplaces in a possibly different way at each workstation. Tasks are assigned to workplaces that must contain the required mounting

<span id="page-4-1"></span>

**Figure 1** An example of an assembly line with 4 stages and two parallel workstations in the second stage. The left side depicts the physical layout of the assembly line. The right side depicts the temporal schedule of several consecutive aircraft. Note that aircraft  $A_i$  and  $A_{i+2}$  go through workstation  $WS_{2,1}$  while  $A_{i+1}$  goes through workstation  $WS_{2,2}$ . This allows each aircraft to spend 2 cycles in stage 2 without conflict.

positions. Two workplaces cannot overlap on the same workstation. In that work, tasks are scheduled inside workstations, and it accounts for extra-long tasks (longer than one cycle time) by splitting them into smaller tasks.

# **2.2 ALB and RCPSP Applied to Aircraft Manufacturing**

The application of operation research techniques to aircraft manufacturing is not novel. In [\[31\]](#page-17-3) the problem of scheduling assembly tasks is formulated as a RCPSP problem. Similarly, [\[5\]](#page-15-9) addresses the problem of assigning operators and models the assembly line as a RCPSP. [\[17\]](#page-16-12) plans the resources for aircraft turnaround operations as an RCPSP problem. However, the concept of workstation is absent from those studies.

In a more closely related approach, [\[26\]](#page-16-13) both allocates tasks to workstations and schedules their executions dates. The model accounts for renewable resources and work zones and scales up to a large number of tasks. [\[27\]](#page-16-14) addresses multi-criteria optimization for the preliminary design of assembly lines. Tasks are both assigned to workstations and scheduled inside them, and work zones and resources such as machines are accounted for. It introduces specific types of constraints such as neutralized zones, and skill exclusion. A simplified version of their use-case was used in the 2023 XCSP3 competition [\[6\]](#page-15-10). While they do not account for parallel workstations, in this paper we modify their use-case in the evaluation section.

# <span id="page-4-0"></span>**3 Aircraft Assembly Line Description**

The production systems involved in aircraft construction are complex as they are usually composed of several factories that can produce one part of the aircraft or assemble several parts together. Each factory has several key areas: storage, offices, preparation areas, assembly lines, locker rooms, etc. In this work, we focus on the final assembly line of a manufacturer of small aircraft. This final assembly line is in charge of assembling the fuselage, the wings, the propeller and the engine, and of installing electrical and hydraulic systems in the aircraft.

As done classically in the aeronautics industry, the assembly line we consider here is composed of several workstations, and is pulsated. In a sequential assembly line, each aircraft stays a given duration in each workstation before going to the next one or going out of the line after the final workstation. This duration, called *cycle time*, and noted *Ct*, is imposed on the manufacturer: it is calculated so as to guarantee a certain number of aircraft delivered per

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month or per year<sup>[2](#page-5-0)</sup>. A set of assembly tasks on the aircraft is performed on each workstation. Tasks are not pre-emptive, i.e. no task can be interrupted, and they cannot overlap several workstations. In fact, no assembly task can be performed while the aircraft moves from one workstation to the next.

In order to be executed, tasks can require equipment and operators with particular skills. Each of them is only available in limited quantity, and we represent it through an abstract cumulative renewable resource. All tools and operators are shared by all the workstations of the assembly line. If a task requires a shared resource in a given workstation, it impacts the resource's available capacity on all other workstations during the task execution. For instance, if a resource with capacity 1 is required for the first task of the first workstation, and if this task has a duration equal to 2, then the resource is not available during the two first time units of each cycle in the entire assembly line.

The aircraft is divided into several zones in which tasks are performed. Two tasks that occupy the same zone cannot be performed simultaneously on the same workstation. Zones are represented as renewable resources of capacity 1; however, unlike equipment and operators, each workstation has its own work zones. This means that two tasks that occupy the same zone, need to be scheduled either one after another on a single workstation, or on different workstations. Note that it would be possible to consider zones with capacity greater than 1.

The assembly line considered in this paper features duplicated workstations that operate in parallel. It is organised as a series of *stages*, each stage being composed of one or several identical workstations that operate in parallel. This allows the aircraft to stay longer in these duplicated workstations. Stages with one workstation behave much like in sequential assembly lines. However, in a stage with *n* parallel workstations, each aircraft stays *n* times the cycle time on its workstation before heading to the next stage. At each cycle, one aircraft goes out of each stage and into the next one. For instance, in the assembly line illustrated in Figure [1,](#page-4-1) the second stage contains two workstations. When entering the second stage, each aircraft  $A_i$  goes either on workstation  $WS_{2,1}$  or workstation  $WS_{2,2}$  and stays there for a duration equal to  $2 \cdot Ct$  in the example. As  $WS_{2,1}$  and  $WS_{2,2}$  are shifted of one cycle time, there is still an aircraft entering  $WS_{3,1}$  at each cycle.

Parallel duplicate workstations allow the aircraft to stay in a stage for a duration greater than the cycle time. This allows for tasks longer than the cycle time to be realized. In the example of Figure [1,](#page-4-1) stages 1, 3 and 4 last one cycle time, but stage 2 lasts two cycles, so it can be assigned a task of a duration up to 2 · *Ct*.

Note that while a stage is physically, or spatially, decomposed into workstations, it would be misleading to divide the time period an aircraft spends on a stage into time periods called "workstations". For example in Figure [1,](#page-4-1) while stage 2 physically has 2 workstations, and while aircraft  $A_i$  stays two cycles in workstation  $WS_{2,1}$ , it would be incorrect to refer to the first half of this time period stay as "the first workstation of stage 2", since aircraft *A<sup>i</sup>* will never travel to workstation  $WS_{2,2}$ . Instead, we say that stage 2 is temporally decomposed into two *substages*, and stages 1, 3 and 4 are composed of one substage.

To summarize, all substages have the same duration of 1 cycle. The assembly line has as many substages as it has workstations. At each instant, each workstation is occupied by exactly one aircraft, and *each aircraft is in a different substage of its construction*. Each aircraft goes through all construction substages in the same order, however it does not necessarily travel through all workstations.

<span id="page-5-0"></span><sup>2</sup> More precisely, the long term production rate, or *takt*, is an objective imposed on the manufacturer. Our work evaluates whether a factory design can reach an operational *cycle time* consistent with the takt under realistic conditions.

<span id="page-6-1"></span>

ച $t_1$ × $t_2$ $t_2$ $t_1$ X $t_3$ × $t_4$ $\times$ $t_3$ $t_4$ $\tau_6$ $t_{5}$ X $t_6$ T7 $\times$ $t_7$ $capa_{1} = 3$ $capa_2 = 2$	Task	Duration		Zone 1 Zone 2 Resource 1 Resource 2	
					$t_{5}$

**Figure 2** Illustration of the simple assembly line problem detailed in Example [2.](#page-7-0)

<span id="page-6-2"></span>

**Figure 3** Illustration of two possible layouts for the problem described in Example [2.](#page-7-0)

Parallel workstations are a useful tool to increase the production rate. They make it possible to schedule tasks that last more than the cycle time, or conversely to increase the production rate so that the cycle time is smaller than the longest assembly task, which is impossible on sequential assembly lines. The drawbacks of parallel workstations are an increased surface occupation, increased tools and operators requirements, and an increased number of aircraft under construction, which can be a significant financial constraint.

When combined with walking workers, parallel workstations can create counter-intuitive situations, where a task seems to use several times its amount of workers. This happens on stages with duplicated workstations, that are assigned a task longer than the cycle time. Assume in Figure [1](#page-4-1) that a task of length 1*.*5 · *Ct* is executed at the start of stage 2, and in theory only requires 1 worker. When aircraft  $A_i$  arrives on workstation  $WS_{2,1}$  the task starts and one worker starts working on it, for the next  $1.5 \cdot Ct$ . One *Ct* later, aircraft  $A_{i+1}$  arrives on workstation  $WS_{2,2}$ , but the worker is still busy on the aircraft  $A_i$ . As a consequence, a task that would only require one worker in a sequential layout, will in fact need two workers in the layout of Figure [1](#page-4-1) and with a cycle time shorter than its duration. This aspect is illustrated later in Example [2](#page-7-0) and Figure [5.](#page-9-0)

Our goal is to study the impact of paralleling workstations on the production rate. To that end, we model the assembly line under several layout assumptions and minimize the cycle time under constant resource capacity assumptions.

### <span id="page-6-0"></span>**4 Assembly Line Model**

This section introduces definitions for our multi-manned assembly line balancing problem with walking workers and parallel stations. The elements constituting such a problem and an associated solution are described first. Then, a constraint programming model in the OPL ([\[20\]](#page-16-15)) language is detailed, explained and illustrated.

## **4.1 Formal Definitions**

▶ **Definition 1** (MALBWP)**.** *A Multi-manned Assembly Line Balancing problem with Walking workers and Parallel stations is a tuple*  $(\mathcal{T}, nS, \mathcal{Z}, \mathcal{R}, \text{Prec}, \text{Occ}, \text{Cons})$  where:

- $\blacksquare$  T is a set of assembly tasks. Each task  $t \in \mathcal{T}$  is associated with its duration dur<sub>t</sub>.
- *nS is the number of stages.* S *denotes the set* [1*..nS*] *of assembly stages, where each stage can have one or more parallel workstations. For each stage*  $s \in S$ *, its number of parallel workstations (and thus substages) is noted ws. The total number of substages is noted*  $W = \sum_{s \in \mathcal{S}} w_s$ .
- $\equiv$  *Z* is a set of work zones representing the different areas of the aircraft, in which workers *can perform different tasks.*
- R *is a set of resources or tools that are needed to perform certain tasks. Each resource*  $\mathbf{r}$  $r \in \mathcal{R}$  *is associated with its capacity capa<sub>r</sub>*.
- *Prec*  $\subseteq$   $\mathcal{T} \times \mathcal{T}$  *is the precedence relation between tasks. For each pair of tasks*  $(t_1, t_2) \in$  *Prec, t*<sup>2</sup> *cannot start until t*<sup>1</sup> *is finished.*
- $\Box$  *Occ*  $\subseteq$   $\mathcal{T} \times \mathcal{Z}$  *is the occupation relation between tasks and zones.* (*t, z*)  $\in$  *Occ means that task t occupies zone z. Each task can use any number of zones.*
- *■* Cons  $\subseteq$   $\mathcal{T} \times \mathbb{N} \times \mathcal{R}$  *is the resource usage relation between tasks and resources.*  $(t, n, r) \in$ *Cons means that task t uses n units of resource r. Each task can use any number of resources. For each resource r*, we denote  $Cons_r$  the set  $\{(t, n) | (t, n, r) \in Cons\}$ , i.e. the *weighted set of tasks that consumes r.*

This model features two different types of resources. Zones have capacity 1, are specific to each workstation, and are present in all workstations. Resources have a finite capacity (possibly 1), however they are shared across all workstations. Note that this type of problem could easily be extended to workstation-specific resources with finite capacity as in [\[26\]](#page-16-13).

<span id="page-7-0"></span>▶ **Example [2](#page-6-1).** Figure 2 describes a small multi-manned assembly line balancing problem with walking workers and parallel stations. The problem contains 7 tasks, 2 zones and 2 resources. The zone occupation relation, the resource consumption relation and the resource capacities are detailed in the table on the left, and the graph induced by the precedence relation is depicted on the right. Resource 1 has capacity 3, and resource 2 has capacity 2.

Figure [3](#page-6-2) illustrates two possible layouts for the assembly line with 3 workstations, i.e.  $W = 3$ . In layout  $L_1$  (Figure [3a\)](#page-6-2) the stage set  $S = \{1, 2, 3\}$  contains 3 stages of 1 workstation each, i.e.  $w_1 = w_2 = w_3 = 1$ . In layout  $L_2$  (Figure [3b\)](#page-6-2), there are two stages  $S = \{1, 2\}$ , and the first one is composed of two workstations, i.e.  $w_1 = 2$  and  $w_2 = 1$ .

▶ **Definition 3** (Solution)**.** *A solution to a given MALBWP is a tuple* (*Ct, start*) *defined as follows.*

- *Ct is the pulse rate of the assembly line.*
- $\blacksquare$  *The duration of stage*  $s \in \mathcal{S}$  *equals*  $w_s$ . *Ct*, the first stage starts at time 0 and each *other stage starts when its predecessor stage ends. The start date of stage s is denoted* stageStart*<sup>s</sup> and its end date is denoted* stageEnd*<sup>s</sup> .*
- $\bullet$  *start associates a start date to each task*  $t \in \mathcal{T}$ , *noted start<sub>t</sub></sub>. The end date of each task, noted end<sup>t</sup> is the sum of its start date and its duration.*
- *Each task is performed inside a unique stage, i.e. for each task*  $t \in \mathcal{T}$ *, there exists a unique stage*  $s \in S$  *such that* stageStart<sub>s</sub>  $\leq$  *start<sub>t</sub>*  $\leq$  *end<sub>t</sub>*  $\leq$  stageEnd<sub>s</sub>.
- $\blacksquare$  *Two tasks that occupy the same zone do not overlap temporally. Formally, if*  $(t_1, z_1) \in Occ$ and  $(t_2, z_2) \in Occ$ , we have either  $t_1 = t_2$ ,  $z_1 \neq z_2$ , end $t_1 \leq start_{t_2}$ , or end $t_2 \leq start_{t_1}$ .

*At each instant, resources are not used beyond their capacity* across all workstations*. Formally, we denote consumption* $(r, \tau)$  *the amount of units of resource r used at instant τ*, and require that at each instant  $\tau \in [0, Ct]$  and for each resource  $r \in \mathcal{R}$ , we have  $\text{consumption}(r, \tau) \leq \text{capa}_r$  *where:* 

<span id="page-8-0"></span>
$$
consumption(r, \tau) = \sum_{(t,n) \in Cons_r} \sum_{k=1}^{W} \begin{cases} n & \text{if start}_{t} \le \tau + k. Ct \le end_t \\ 0 & \text{otherwise} \end{cases}
$$
 (1)

The last constraint on the consumption of resources across workstations is, to our knowledge, original. At each instant, there is one aircraft in each assembly substage, thus each resource is used simultaneously by each aircraft. Hence, in Equation [\(1\)](#page-8-0), in the second summation, *k* ranges across all substages (*W* is the total number of substages). Moreover, in a stage with multiple workstations, a task may last longer than one *Ct*. In this case, this task is performed simultaneously on multiple workstations, and contributes to the resource usage multiple times. This is captured by the  $\tau + k \cdot Ct$  term of Equation [\(1\)](#page-8-0), and illustrated in the example that follows.

▶ **Example [4](#page-9-1).** Figures 4 and [5](#page-9-0) depict two solutions for the problem of Example [2](#page-7-0) respectively associated with layout  $L_1$  and layout  $L_2$ .

With layout  $L_1$  (Figure [4\)](#page-9-1), the best possible cycle time is 8. Zone occupation and resource usage can be visualized by the intervals depicted in the figure. Since each aircraft instance has its own work zones, each zone is duplicated in each stage and substage. On the opposite, resources are shared between all assembly stages, and are used simultaneously. Thus, the aircraft in stage 1 consumes 2 units of resource 1, at the same time as the aircraft in stage 2 consumes 1 unit of it. The same holds for all stages, resources and tasks. This is illustrated on the right hand side of Figure [4.](#page-9-1)

In Figure [5,](#page-9-0) the stage set  $S = \{1, 2\}$  contains two stages, however the first stage contains two parallel workstations and therefore two substages. This means we have  $w_1 = 2$ , and  $w_2 = 1$ , which allows task  $t_2$  to start at date 2 in substage 1.1 and end at date 8 in substage 1*.*2 (as tasks cannot span across stages, but can span across substages). In this setting, the smallest cycle time is 6.

A feature specific to our approach is how task *t*<sup>2</sup> consumes resource 1 "several times" in Figure [5.](#page-9-0) At each instant, 3 aircraft are being assembled, one in each assembly stage. Let us consider the moment when 3 time units have elapsed in the current cycle and that aircraft  $A_i$ ,  $A_{i-1}$  are in stage 1, and aircraft  $A_{i-2}$  in stage 2.  $A_i$  has already spent 3 time units in stage 1, meaning that 1 time unit of task  $t_2$  has already been executed for it. Aircraft  $A_{i-1}$  is also in stage 1 but has entered it  $Ct + 3 = 9$  time units ago and is therefore in substage 1.2. Task  $t_2$  is not yet finished for this aircraft, as 7 time units have already been executed. Consequently, two instances of the task  $t_2$  are simultaneously executed on two different aircraft instances, thus using twice the amount of resource 1, as illustrated on the right. Note that at the same time, task  $t_6$  is executed on aircraft  $A_{i-1}$  in stage 2, consuming an additional unit of resource 1.

## **4.2 Constraint Programming Representation**

The MALBWP problem can be represented in CP provided that an upper bound on the pulse time *maxCt* is added to the problem inputs. The resulting temporal horizon considered, denoted *H*, is equal to  $\sum_{s \in \mathcal{S}} w_s \cdot maxCt$ .

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<span id="page-9-1"></span>

<span id="page-9-0"></span>**Figure 4** Solution with  $Ct = 8$  for the toy problem with layout  $L_1$ .



**Figure 5** Solution with  $Ct = 6$  for the toy problem with layout  $L_2$ .

Our representation makes use of the OPL constraint programming language [\[33\]](#page-17-4), as it makes heavy use of its concept of interval variables and associated constraints, in particular for optional intervals. An interval variable has a range [*min, max*] that specifies its earliest start date and latest end date, and it can be optional, meaning in that case that it is not necessarily present in the produced schedule. The OPL functions startOf, endOf, and lengthOf return respectively the start date, end date and length of a given interval.

Our constraint model uses the following decision variables. All interval variables have the range [0*, H*] unless explicitly mentioned.

- The rate is modeled by an integer variable **Ct** in the range [0*, maxCt*]. m.
- For each task  $t \in \mathcal{T}$ , the mandatory interval variable **itv**<sub>*t*</sub> represents the execution of task  $\overline{a}$ *t*. Its duration is fixed to *dur<sup>t</sup>* .
- For each task  $t \in \mathcal{T}$  and each stage  $s \in \mathcal{S}$ , the optional interval variable  $\mathbf{itv}_{t,s}$ . If present, this interval has the fixed duration *dur<sup>t</sup>* , and represents the execution of task *t* in stage *s*. If absent, it means task *t* is executed in another stage.
- For each task  $t \in \mathcal{T}$ , each stage  $s \in \mathcal{S}$  and each substage  $w \in [1..w_s]$ , the optional interval variable **itv**<sub>t,s,w</sub> represents the execution of task t during the w-th substage of stage *s*. If absent, it means that task *t* is executed on another stage or substage. For example in Figure [5,](#page-9-0) task  $t_1$  is executed during the first substage of the first stage. Hence  $\mathbf{itv}_{t_1,1,1}$  is present, and both  $\mathbf{iv}_{t_1,1,2}$  and  $\mathbf{iv}_{t_1,2,1}$  are absent. On the other hand, task  $t_2$  is executed in both substages of stage 1, so  $\mathbf{iv}_{t_2,1,1}$  and  $\mathbf{iv}_{t_2,1,2}$  are both present and  $\mathbf{iv}_{t_2,2,1}$  is absent.
- For each task  $t \in \mathcal{T}$ , each stage  $s \in \mathcal{S}$  and each substage  $w \in [1, w_s]$ , the optional interval  $\sim$ variable **itvRsc**<sub>*t,s,w*</sub> has a range equal to  $[0, maxCt]$ . It represents the time during which the execution of time *t* on the *w*-th substage of station *s* uses its resources. If task *t* is executed on another stage, or not during the *w*-th substage, this interval is absent. As for the previous set of interval variables, **itvRsc**<sub>*t*<sub>1</sub>,1,1</sub>, **itvRsc**<sub>*t*<sub>2</sub>,1,1</sub> and **itvRsc**<sub>*t*<sub>2</sub>,1,2</sub> are present, whereas **itvRsc**<sub>*t*<sub>1</sub>,2,1</sub> and **itvRsc**<sub>*t*<sub>2</sub>,2,1</sub> are absent.

The constraints of our encoding are as follows.

## **Assembly Line Expressions**

We start by defining some expressions for the start and end dates for each stage, and for each pulse cycle inside each stage. Expressions are neither constraints nor decision variables, but are useful for efficiently expressing recurrent patterns in constraints.

<span id="page-10-1"></span><span id="page-10-0"></span>
$$
\forall s \in \mathcal{S}, \qquad \qquad \text{stageStart}_s = \sum_{i=1}^{s-1} w_i \cdot \mathbf{C} \mathbf{t} \tag{2}
$$

<span id="page-10-3"></span><span id="page-10-2"></span>
$$
\forall s \in \mathcal{S}, \qquad \text{stageEnd}_s = \sum_{i=1}^s w_i \cdot \mathbf{C} \mathbf{t} \tag{3}
$$

$$
\forall s \in \mathcal{S}, \forall w \in [1...w_s], \qquad \text{subStageStart}_{s,w} = \text{stageStart}_s + (w - 1) \cdot \text{Ct} \tag{4}
$$

$$
\forall s \in \mathcal{S}, \forall w \in [1...w_s], \qquad \qquad \text{subStageEnd}_{s,w} = \text{stageStart}_s + w \cdot \text{Ct} \tag{5}
$$

Equations [\(2\)](#page-10-0) and [\(3\)](#page-10-1) define expressions for the start and end dates of each stage. These dates directly depend on the value of **Ct**. Similarly, Equations [\(4\)](#page-10-2) and [\(5\)](#page-10-3) define expressions for the start and end date of every substage.

Note that stageStart<sub>1</sub> = 0, which means the first stage starts at time 0. Moreover,  $\text{stageStart}_{s+1} = \text{stageEnd}_s$  for all stages except for the last one, which means each stage starts right when its predecessor ends. Finally, a stage with *w<sup>s</sup>* workstations has a duration of *w<sup>s</sup>* · *Ct*, and each substage has a duration of exactly one *Ct*.

## **Stage and Zone Constraints**

The stage constraints express that each task can only take place inside one stage. Furthermore, two tasks that occupy the same zone cannot be executed at the same time in the same stage. This set of constraints features the alternative OPL keyword, that accepts one interval *a* and one set of optional intervals *B* as arguments, and has the following semantics. If *a* is absent, then all intervals in *B* are absent. If *a* is present, then exactly one interval is *B* is present, and it has the same start and end dates as *a*.

$$
\forall (t, t') \in \text{Prec}, \qquad \qquad \text{endBeforeStart}(\mathbf{itv}_t, \mathbf{itv}_{t'}) \tag{6}
$$

 $\forall z \in \mathcal{Z}$  noOverlap $(\{\mathbf{itv}_t | (z,t) \in \mathit{Occ}\})$ (7)

<span id="page-10-8"></span><span id="page-10-7"></span><span id="page-10-6"></span><span id="page-10-5"></span><span id="page-10-4"></span> $\forall t \in \mathcal{T},$  alternative $\left(\mathbf{it} \mathbf{v}_t, \{\mathbf{it} \mathbf{v}_{t,s} | s \in [1..nS]\}\right)$ (8)

$$
\forall t \in \mathcal{T}, \forall s \in \mathcal{S}, \qquad \qquad \text{stageStart}_{s} \leq \text{startOf}(\textbf{itv}_{t,s}, H) \tag{9}
$$

$$
\forall t \in \mathcal{T}, \forall s \in \mathcal{S}, \qquad \qquad \text{endOf}(\textbf{itv}_{t,s}, 0) \leq \text{stageEnd}_s \tag{10}
$$

Constraints [\(6\)](#page-10-4) expresses that two tasks with a precedence constraint must be scheduled in the proper order. Constraints [\(7\)](#page-10-5) forbid two tasks that occupy the same zone to be executed simultaneously in the same stage. Constraints  $(8)$  force interval  $\mathbf{iv}_t$  to equal exactly one of the **itv**<sub>*t,s*</sub>. Constraints [\(9\)](#page-10-7) and [\(10\)](#page-10-8) ensure that each interval **itv**<sub>*t,s*</sub>, if present, takes place during stage *s*. The second arguments to the startOf and endOf functions (respectively *H* and 0) are default values in case the interval is absent. In conjunction to Constraints [\(8\)](#page-10-6), this enforces every task to be executed inside one stage.

## **Substage Constraints**

We consider here constraints associated with the execution of tasks in substages.

$$
\forall t \in \mathcal{T}, \forall s \in [1..nS], \qquad \qquad \text{span}\big(\textbf{itv}_{t,s}, \{\textbf{itv}_{t,s,w}|w \in [1..w_s]\}\big) \tag{11}
$$

$$
\forall t \in \mathcal{T}, \forall s \in \mathcal{S}, \forall w \in w_s, \qquad \text{subStageStart}_{s,w} \le \text{startOf}(\text{itv}_{t,s,w}, H) \tag{12}
$$

$$
\forall t \in \mathcal{T}, \forall s \in \mathcal{S}, \forall w \in w_s, \qquad \qquad \text{endOf}(\text{itv}_{t,s,w}, 0) \leq \text{subStageEnd}_{s,w} \tag{13}
$$

<span id="page-11-3"></span><span id="page-11-2"></span><span id="page-11-1"></span><span id="page-11-0"></span>
$$
\forall t \in \mathcal{T}, \forall s \in \mathcal{S}, \qquad \qquad \text{lengthOf}(\textbf{itv}_{t,s}) = \sum_{w \in [1..w_s]} \text{lengthOf}(\textbf{itv}_{t,s,w}) \qquad (14)
$$

Constraints [\(11\)](#page-11-0) uses the span OPL constraint, that accepts an interval *a* and a set of optional intervals *B* as arguments, and has the following semantics. If *a* is absent, then all intervals in *B* are absent. If *a* is present, then at least one interval in *B* is present and the start (resp. end) date of *a* equals the earliest start date (resp. latest end date) of all the present intervals in *B*.

Constraints [\(12\)](#page-11-1) and [\(13\)](#page-11-2) ensure that each interval  $\mathbf{iv}_{t,s,w}$ , if present, lies within the w-th substage. As a consequence, for each task  $t$  and stage  $s$ , two intervals  $\mathbf{itv}_{t,s,w}$  and **itv**<sub>*t*,*s*,*w*<sup>*'*</sup> cannot overlap, unless  $w = w'$ .</sub>

Constraints [\(14\)](#page-11-3) ensure that the cumulated length of all present  $\mathbf{itv}_{t,s,w}$  intervals equals the length of their spanning  $\mathbf{iv}_{t,s}$  interval. Since they cannot overlap because of Constraints [\(12\)](#page-11-1) and [\(13\)](#page-11-2), this ensures that the intervals  $\mathbf{iv}_{t,s,w}$  entirely cover the interval  $\mathbf{iv}_{t,s}$  without any gaps, for each task *t* and stage *s*.

## **Resource Constraints**

As illustrated in Example [2,](#page-7-0) resource usage is a pattern that is repeated every cycle. In order to model resource usage across workstations with interval variables, we rewrite the consumption of resources of Equation [\(1\)](#page-8-0) as a consumption elements set. Formally, for each resource *r*, we consider the set  $\mathcal{C}_r$  of consumption elements  $\{(\sigma, \tau, n)\}\$  where  $\sigma$  is the start date of the consumption,  $\tau$  is its end date and *n* the number of units it consumes.  $\mathcal{C}_r$  is built following Algorithm [1,](#page-11-4) where  $\%$  is the integer remainder.

<span id="page-11-4"></span>**Algorithm 1** Computation of C*<sup>r</sup>* for each resource *r*.

```
1: function computeCr(r)2: \mathcal{C}_r \leftarrow \emptyset3: for (t, n) \in \text{Cons}_r do
4: if (dur_t \leq Ct) \wedge (start_t \mathcal{C} Ct + dur_t \leq Ct) then
5: \mathcal{C}_r \leftarrow \mathcal{C}_r \cup \{(\text{start}_t \,\% \, Ct, \text{end}_t \,\% \, Ct, n)\}6: else
7: before \leftarrow Ct - start \cdot \% Ct8: after \leftarrow end_t \% Ct9: q \leftarrow \lfloor \frac{dur_t - before - after}{Ct} \rfloor10: \mathcal{C}_r \leftarrow \mathcal{C}_r \cup \{ (start_t \% Ct, Ct, n), (0, end_t \% Ct, n), (0, Ct, n \cdot q) \}{\mathcal C}_r
```
 $\mathcal{C}_r$  is initialized to the emptyset. Then, for each tuple  $(t, n) \in \mathcal{C}$ ons<sub>*r*</sub>, we add one or several elements to  $\mathcal{C}_r$ . If the task is contained in a unique substage (Line [4\)](#page-11-4), then we shift the task so that it is contained in the interval [0*, Ct*]. Otherwise, we consider the duration of the task in its first substage (Line [7\)](#page-11-4) and in its last substage (Line [8\)](#page-11-4). Then, the quotient

∀*r* ∈ R*,*

*q* (Line [9\)](#page-11-4) denotes the number of substages over which the task completely spans. We finally add consumptions corresponding to the first substage, to the last and all the included substages, and shift them so that they belong to the interval [0*, Ct*].

The OPL encoding uses the **itvRsc**<sub>*t,s,w*</sub> intervals to represent the tuples from the  $C_r$ set for each resource. The integer remainder operator can be eluded thanks to the  $\mathbf{iv}_{t,s,w}$ intervals.

$$
\forall t \in \mathcal{T}, \forall s \in \mathcal{S}, \forall w \in w_s, \qquad \qquad \text{endOf}(\mathbf{itv} \mathbf{Rs} \mathbf{c}_{t,s,w}, 0) \leq \mathbf{Ct} \tag{15}
$$

$$
\forall t \in \mathcal{T}, \forall s \in \mathcal{S}, \forall w \in w_s, \qquad \text{presenceOf}(\mathbf{itvRsc}_{t,s,w}) = \text{presenceOf}(\mathbf{itv}_{t,s,w}) \tag{16}
$$

$$
\forall t \in \mathcal{T}, \forall s \in \mathcal{S}, \forall w \in w_s, \qquad \text{startAtStart}(\textbf{itvRsc}_{t,s,w}, \textbf{itv}_{t,s,w}, \text{subStageStart}_{s,w}) \tag{17}
$$

$$
\forall t \in \mathcal{T}, \forall s \in \mathcal{S}, \forall w \in w_s, \qquad \qquad \mathsf{endAtEnd}(\mathbf{itvRsc}_{t,s,w}, \mathbf{itv}_{t,s,w}, \mathsf{subStageStart}_{s,w}) \tag{18}
$$

<span id="page-12-5"></span><span id="page-12-4"></span><span id="page-12-3"></span><span id="page-12-2"></span><span id="page-12-1"></span>
$$
\sum_{(t,n)\in Cons_r} \text{pulse}(\textbf{itvRsc}_{t,s,w}, n) \leq capa_r \tag{19}
$$

Constraints [\(15\)](#page-12-1) ensure that resource usage intervals fall into the [0*,* **Ct**] time frame, since they represent the use of each resource at each substages. Constraints [\(16\)](#page-12-2) impose that the only resource usage intervals that are present are those that correspond to a present substage interval for this task. Constraints [\(17\)](#page-12-3) and [\(18\)](#page-12-4) use the startAtStart and endAtEnd OPL constraints with delay. When both intervals are present they are respective shorthands for startOf( $\textbf{itvRsc}_{t,s,w}$ ) + subStageStart<sub>s,*w*</sub> = startOf( $\textbf{itv}_{t,s,w}$ ), and  $\text{endOf}(\textbf{itvRsc}_{t,s,w}) + \text{subStageStart}_{s,w} = \text{endOf}(\textbf{itv}_{t,s,w})$ , and do nothing if at least one interval is absent.

The final constraints [\(19\)](#page-12-5) use the pulse OPL function, that represents a cumulative function. The pulse primitive accepts an interval *a* and an integer value *h*, and describes the function with value *h* in interval *a* (if present), and 0 elsewhere. Constraints [\(19\)](#page-12-5) ensure that the resources consumed by all tasks at some time during the range [0*, Ct*] do not exceed the resource capacity.

Figure [6](#page-13-0) illustrates the relationship between all interval variables used in the CP model. Absent intervals are grayed, mandatory intervals have a thick border. A dotted line between two intervals indicates that they are connected by the constraints indicated on the right. This figure represents a [3*,* 1] layout, i.e. a layout with 3 workstations in the first stage and one in the second stage, and a single task *t* that uses *n* units of a resource *r* with capacity 3*n*. Interval **itv**<sub>*t*</sub> ranges across the whole planning horizon. Each interval **itv**<sub>*t*,s</sub> ranges across stage *s*. Each interval  $\mathbf{itv}_{t,s,w}$  ranges across substage *s, w*. Intervals  $\mathbf{itvRsc}_{t,s,w}$  all range across [0*,* **Ct**].

# <span id="page-12-0"></span>**5 Experimentation**

This section presents experimental results for the constraint model presented in this paper. Experiments were all run using IBM CP Optimizer 20.1.0 through the Java API on a 20-core Intel(R) Xeon(R) CPU E5-2660 v3  $@$  2.60GHz with 62GB RAM. We first present results obtained on PSPLib-based benchmarks, then results obtained on two real assembly line datasets.

## **5.1 Results for PSPLib-based Benchmarks**

Our model can be seen as an extension of a classical RCPSP problem (A*,* R*es,* C*ons,*P*rec*), where A is the set of activities, Res the set of resources, Cons the consumption relationship and P*rec* the precedence relationship. Given a set of stages {1*..nS*} and their associated

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<span id="page-13-0"></span>

**Figure 6** Illustration of the interval variables hierarchy in the CP model.

number of workstations, and a RCPSP problem, we consider the associated MMALBWP  $(A, nS, \emptyset, \mathcal{R}es, \mathcal{P}rec, \emptyset, \mathcal{C}ons)$  in which the set of zones is empty. The resulting dataset will be made public upon acceptance of the paper.

We adapted problems from the PSPLib [\[19\]](#page-16-3), converting them to MMALBWP by adding a stage size specification, and used a time limit of 5 minutes. The results on the 150 first problems of the j30rcp benchmark, detailed in appendix [A,](#page-17-5) demonstrate several results summarized in Table [1a,](#page-14-1) and in graph in Table [1b](#page-14-1) that counts the number of instances where some layouts *strictly* improve other layouts. Below are additional considerations.

- $\blacksquare$ With one workstation, the cycle time equals the RCPSP makespan.
- More workstations always means a better or equal cycle time (in sequence or in parallel).  $\overline{a}$
- Adding a second workstation, in sequence (layout [1*,* 1]) or in parallel (layout [2]), always  $\overline{a}$ strictly improves the cycle time of layout [1].
- $\blacksquare$  Adding a third workstation further improves the cycle time in 62% of sequential instances (93*/*150, [1*,* 1*,* 1] compared to [1*,* 1]), and in 51% of parallel instances ((74 + 78)*/*300, [2*,* 1] and  $[1, 2]$  compared to  $[2]$ ).
- At constant number of workstations, merging two stages yields a better cycle time in 25%  $\blacksquare$ of instances  $((69 + 23 + 21)/450, [2]$  compared to [1, 1] plus [2, 1] and [1, 2] compared to  $[1, 1, 1]$ ). This lower number is explained by the fact that resources become a limiting factor.
- Adding stages makes the problem more difficult for the solver, especially with parallel workstations.

**(a)** Results summary.



<span id="page-14-1"></span>**Table 1** Statistics of the solutions of the first 150 instances of the j30rcp PSPLIB benchmark.

**(b)** Number of instances in which a layout yields a strictly better cycle time than another.



# **5.2 Industrial Assembly Line**

This work was initially motivated by the final assembly line of a manufacturer of small aircraft, who already uses a parallel workstation.

The assembly line model contains 51 tasks, 93 precedence constraints, 12 work zones, and 23 resources. Tasks use between 1 and 4 resource units. We have tested two factory layouts: the first one has 5 sequential stages of one workstation each  $([1, 1, 1, 1, 1])$ ; the second layout duplicates the first workstation in parallel ([2*,* 1*,* 1*,* 1]). We solved the model with a 30 minutes timeout. The solver finds its best solution in a few seconds, but fails to prove that the solution is optimal in the rest of its computation time. The results, and in general the entire modeling work, helped analyse and consolidate the different factory configurations. Note that due to confidentiality issues, we do not provide more information about this dataset nor the obtained results.

# **5.3 Assembly Line Design Use Case**

We tested our approach on the assembly line preliminary design problem originally dataset [\[28\]](#page-16-16) presented in [\[27\]](#page-16-14) and adapted for the 2023 XCSP3 competition [\[6\]](#page-15-10). In the latter, the assembly line is composed of 4 stages with one workstation each. The objective is to minimize the number of operators in the line. We adapt these benchmarks by fixing the number of available operators, ignoring neutralization constraints, and optimizing the cycle time when considering several factory layouts. We have considered 7 layouts for each of the three instances, resulting in a dataset of 21 instances available online [\[29\]](#page-17-0).

These instances are larger and much more challenging than the ones presented before. We gave a 10mn time limit to the solver, it only managed to find a solution for 15 instances out of 21, and it still failed to prove the optimality of all instances, as reported in Table [2.](#page-15-11) Furthermore, in instance 3, the cycle time found for stage sizes [1*,* 3] (i.e. two stages of respectively 3 and 1 workstations) is higher than the one found for stage sizes [1*,* 2*,* 1], which indicates that the solver failed to find the same solution within its time limit.

## <span id="page-14-0"></span>**6 Conclusion**

This paper presents a Constraint Programming model for a multi-manned assembly line balancing problem with walking workers and parallel stations. This model can be seen as an extension of multi-manned ALP with walking workers, and of RCPSP with parallel workstations. It provides an efficient way to evaluate how the number of workstations and

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<span id="page-15-11"></span>**Table 2** Production rates associated to each dataset and various layouts. Empty cells indicate that the 10 minutes timeout elapsed before a solution was found.

their flow can help design a factory, by evaluating the production rates that it can attain. We detailed a constraint programming model in the OPL language that makes extensive use of interval variables, and validated the model by reproducing classical RCPSP benchmark results. We also used it to study assembly line implementations for industrial use cases.

There are several directions for pursuing this work. Better heuristics would probably improve the solver performance on the PSPLIB-based instances. Moreover, it would be interesting to address both workstation layout and resource sizing in a multi-objective approach. This would pave the way for the design of assembly lines with different configurations for low rate with low resource consumption, and high rates with high resource consumption.

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# <span id="page-17-5"></span>**A PSPLib Benchmarks Results**

This appendix presents the results of execution of our solver on the first 150 instances of the PSPLib benchmark j30rcp. Table [3](#page-17-6) is read as follows: Parameter value *p* and Instance value *i* describe the problem found in the file named  $J30p_i$ .RCP in the archive named j30rcp.zip found in the PSPlib [\[19\]](#page-16-3) accessible at url [https://www.om-db.wi.tum.](https://www.om-db.wi.tum.de/psplib/getdata_sm.html) [de/psplib/getdata\\_sm.html](https://www.om-db.wi.tum.de/psplib/getdata_sm.html). Each column in the "Cycle time" and "Computation time" groups corresponds to a different layout. A cycle time of −1 indicates that the solver failed to find a suitable schedule in the allocated time of 5 minutes. A computation time of 300 seconds (5 minutes) indicates that the solver used all its computation time. This means that the corresponding cycle time (if any) may be sub-optimal.



<span id="page-17-6"></span>**Table 3** Detailed Results of the 150 first instances of the j30rcp benchmark for various number of stages and workstations.





