Systematic Testing of Embedded Automotive Software: The Classification-Tree Method for Embedded Systems (CTM/ES)

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The software embedded in automotive control systems increasingly determines the functionality and properties of present-day motor vehicles. The development and test process of the systems and the software embedded becomes the limiting factor. While these challenges, on the development side, are met by employing model-based specification, design, and implementation techniques [KCF+04], satisfactory solutions on the testing side are slow in arriving. With regard to the systematic selection (test design) and the description of test scenarios especially, there is a lot of room for improvement. Thus, a main goal is to effectively minimize these deficits by creating an efficient procedure for the selection and description of test scenarios for embedded automotive software and its integration in the model-based development process.

The realization of this idea involves the combination of a classical software testing procedure with a technology, prevalent in the automotive industry, which is used for the description of time-dependent stimuli signals. The result of this combination is the classification-tree method for embedded systems, CTM/ES [Con04].

The classification-tree method for embedded systems complements model-based development by employing a novel approach to the systematic selection and description of the test scenarios for the software embedded in the control systems. CTM/ES allows for the graphic representation of time-variable test scenarios on different levels of abstraction: A problem-oriented, compact representation, adequate for a human tester and containing a high potential for reusability, is gradually being transformed into a solution-oriented technical representation which is suited for the test objects' stimulation. The CTM/ES notation facilitates a consistent representation of test scenarios which may result from different test design techniques. The test design technique which this method is primarily based on, is a data-oriented partitioning of the input domain in equivalence classes. Secondary test design techniques are, for instance, the testing of specific values (or value courses) or requirement-based testing.

Within a model-based development process for embedded automotive software CTM/ES can be combined with structural testing on the model level in order to form an effective test strategy for model-based testing. Systems which have been developed in a model-based way can be tested systematically and efficiently by means of the CTM/ES and the tools based thereon, such as the classification-tree editor for embedded systems CTE/ES [Mis04, CTE/ES], as well as the model-based test environment MTest [LBE+04, MTest].

INTRODUCTION

The rapid increase in the software complexity of today's ECUs makes testing a central and significant task within embedded automotive software development. *Dynamic Testing*, meaning the execution of a test object with selected (sequences of) test data for the analysis of behavior in these cases, is the most important and most widespread measure for software quality assurance.

Since exhaustive testing is impossible in practice, dynamic testing is always a sampling procedure. A subset of test scenarios¹ which is as small as possible and which is able to reveal as many errors in the test object as possible, needs to be selected from the complete set of possible test scenarios. The selection of adequate sample elements (test scenarios) decides on the extent and quality of the whole test.

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¹ General description of a test case which specifies a set of (detailed) test data or test data sequences.

Because of the exceptional significance of the selection process there is a need for *systematic* techniques which support the tester when designing adequate test scenarios. The application of these *test design techniques*² systematizes the selection of test scenarios and makes it both comprehensible and reproducible [Sim97].

In addition to the test design techniques, there is a need for suitable *notations for* the documentation of the selected *test scenarios*. The automatic creation of test scenarios of functional tests is desirable but only possible to a limited extent. As a result, test design must largely be carried out manually. It requires a test (scenario) notation which caters for the human tester.

When testing embedded automotive software, an *ad-hoc selection of test scenarios* is typical, i.e. the test scenarios are determined without (explicitly) defined test design techniques. This selection depends on the experience and expertise of the tester and can only be reproduced with difficulty, if at all. An ad-hoc selection of test scenarios leads, on the one hand, to test scenarios which are more or less redundant and, on the other hand, to test gaps. As a rule, it is impossible to make statements as to the quality or completeness of the ad-hoc test scenarios. With regard to the number of test scenarios, their level of error detection is often inadequate.

The test notations which are often used when developing automotive control software, such as the direct description of the test scenarios in the form of time-dependent value courses or the use of test scripts, lead to a description of test scenarios on a very low level of abstraction, making maintainability and reuse difficult. In addition, the methodical support for the use of the notations is insufficient.

In order to effectively minimize these deficits, the \underline{C} lassification- \underline{T} ree \underline{M} ethod for \underline{E} mbedded \underline{S} ystems (CTM/ES), an efficient procedure for the selection and description of test scenarios for embedded automotive software, was developed.

THE CLASSIFICATION-TREE METHOD FOR EMBEDDED SYSTEMS (CTM/ES)

The CTM/ES [CDF+99, Con04], an extension of the Classification-Tree Method [GG93], is a black-box test design technique which allows systematic test design for embedded systems and their control software as well as a comprehensive graphical description of time-dependent test scenarios by means of abstract signal waveforms that are defined stepwise for each input. The basic concept is to split up the input domain of the test object according to different aspects usually corresponding to different input data sources. The different partitions, called classifications, are subdivided into (input data) equivalence classes. Finally, different combinations of input data classes are selected and arranged into test sequences.

The starting point for the proposed systematic test design approach is an interface description of the test object. Based on this, and by using the Classification-Tree Method for Embedded Systems, the tester can derive test scenarios systematically and describe them graphically.

Example PedInt

For illustration p

For illustration purposes a simplified component is used to interpret the pedal positions within a passenger car. This subsystem can be employed as a preprocessing component for various vehicle control systems. The pedal interpretation (short: PedInt) subsystem interprets the current, normalized positions of accelerator and brake pedal (phi_Acc, phi_Brake) by using the actual vehicle speed (v_act) as desired torques for drivetrain and brake (T_des_Drive, T_des_Brake). Furthermore, two flags (AccPedal, BrakePedal) are calculated which indicate whether or not the pedals are considered to be depressed.

The software requirements on the PedInt component are summarized in Tab 1. For the following considerations, those sub-functions of PedInt should be tested which both flags calculate (SR-PI-01.x). Fig. 1 shows the functional Simulink model for the PedInt component which was developed based on the requirements.

² Standardized procedures for selecting test scenarios on the basis of certain sources of information (e.g. functional specification, executable model, program code).

Table 1: Software requirements specification of PedInt (excerpt)

ID	Description
SR-PI-01	Recognition of pedal activation If the accelerator or brake pedal is depressed more than a certain threshold value, this is indicated with a pedal-specific binary signal.
SR-PI-01.1	Recognition of brake pedal activation If the brake pedal is depressed more than a threshold value ped_min, the BrakePedal flag should be set to the value 1, otherwise to 0.
SR-PI-01.2	Hysteretic behavior of brake pedal activation No hysteresis is to be expected during brake pedal activation recognition.
SR-PI-01.3	Recognition of accelerator pedal activation If the accelerator pedal is depressed more than a threshold value ped_min, the AccPedal flag should be set to the value 1, otherwise to 0.
SR-PI-01.4	Hysteretic behavior of accelerator pedal activation No hysteresis is to be expected during accelerator pedal activation recognition.
SR-PI-02	Interpretation of pedal positions Normalized pedal positions for the accelerator and brake pedal should be interpreted as desired torques. This should take both comfort and consumption aspects into account.
SR-PI-02.1	Interpretation of brake pedal position []
SR-PI-02.2	Interpretation of accelerator pedal position []

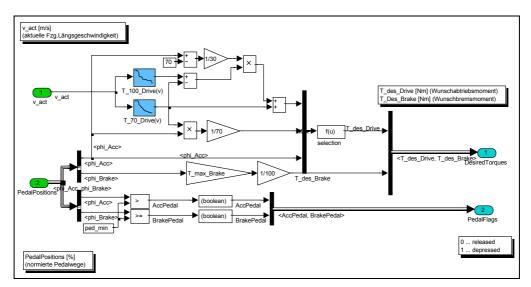


Figure 1: Simulink model of PedInt component

TEST INTERFACE DETERMINATION

In the first step, the interface which is relevant for the test has to be determined. Let the basis of further considerations be a test object with n inputs to be stimulated and m outputs to be observed. The input interface I of the test object is represented via the input variables i_i (i=1,...,n), the output interface O via the output variables o_j (i=1,...,m). The variables actually used for the test are referred to as *effective test interface* I \cup O.

In order to test the test object, each input variable of the effective test interface has to be stimulated with a time-variant signal. The output variables have to be recorded. Thus, the effective test interface determines the *signature of the test scenarios* belonging to it:³

Example PedInt

Table 2 describes the effective test interface $I \cup O$ of the PedInt component.

Table 2: Interface description of PedInt

variable	\leftrightarrow	unit	domain	data type
v_act	•	m/s	[-10,70]	real
phi_Brake	•	%	[0,100]	real
phi_Acc	•	%	[0,100]	real
T_des_Drive	•	Nm		real
T_des_Brake	⋖	Nm		real
AccPedal	◄	_	{0,1}	bool
BrakePedal	•	_	{0,1}	bool

Thus, the signature for the fundamental functional test scenarios is as follows:

I:
$$phi_Acc^*$$
, phi_Brake^* : $Time \rightarrow R|_{[0,100]}$ O: $T_des_Drive^*$, $T_des_Brake^*$: $Time \rightarrow R$ v_act^* : $Time \rightarrow R|_{[-10,70]}$ AccPedal*, BrakePedal*: $Time \rightarrow \{0,1\}$

TEST INPUT PARTITIONING

In a second step, the admissible values of the signals which form the effective input interface must be disjointedly and completely partitioned into (equivalence) classes which are suitable abstractions of individual input values for testing purposes. The partitioning is graphically represented by means of a *classification-tree*.

The partitioning aims to achieve a selection of the individual *equivalence classes* in such a way that they behave homogeneously with respect to the detection of potential errors. That is, the test object behaves either correctly or erroneously for all the values of one class (*uniformity hypothesis*).

A heuristic procedure has proved successful in approaching this ideal partitioning as much as possible in practice. The inputs' data types and value ranges provide the first valuable clues to partitioning: where real-valued data types with minimum and maximum values established are concerned, it is possible, for example, to create a standard class each for the boundary values, for the value of zero and for those intervals in between [Con04]. In general, the data type-specific, standard classifications are not detailed enough for a systematic test. They have to be refined or modified manually in order to approach partitioning according to the uniformity hypothesis.

Example PedInt

According to standard partitioning for real-valued signals, the pedal positions which can take values from the range of 0% to 100%, would be partitioned into 3 classes 0,]0, 100[, and 100. For the vehicle speed the five partitions -10,]-10,0[, 0,]0,70[, and 70 are obtained⁴.

³ I_i and O_i are the sets of admissible values for the variables i_i and o_i respectively.

⁴ [x, y] denotes an interval, closed on both sides,]x, y[an interval, open on both sides, with the boundary values x and y.

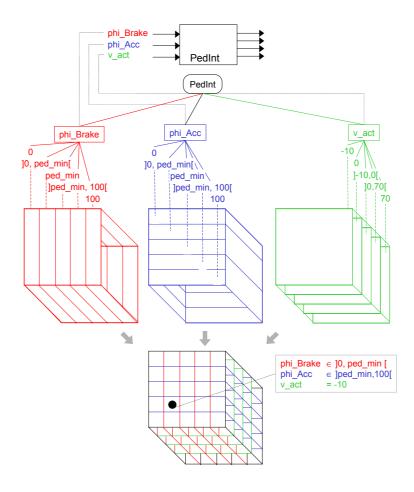


Figure 2: From test interfaces to classification-trees

As v_act is only of secondary importance for testing those sub-functions of PedInt which both flags calculate, the standard classification has not been modified there. On the other hand, the evaluation of the pedal positions recognizes a pedal as depressed only if it is activated above a certain threshold value ped_min. Therefore, the pedal values above and below the threshold should be considered separately because behavior is expected to differ. A class has also been added for the exact threshold value. In order to keep the classification-tree flexible, parameter names were partly used as class boundaries rather than fixed values. The result is a final partitioning of the pedal positions into the classes 0,]0, ped min,]ped min,]ped min, 100[, and 100.

The partitioning into classes is visualized by means of a classification-tree (Fig. 2, middle part). The lower part of Fig. 2 shows how the equivalence classes for the individual input variables partition the input domain of the entire test object.

TEST SCENARIO DETERMINATION

In the third step, test scenarios are determined based on the test input partitioning. The test scenarios describe the course of these inputs over time in a comprehensive, abstract manner. Each scenario captures a data abstraction of the test object's inputs and thus describes – largely independent of detailed test data – what is to be tested.

The classification-tree is used to define the columns of a *combination table*. In order to represent test scenarios in an abstract way, they are decomposed into individual *test steps*. These compose the rows of the combination table according to their temporal order (Fig. 3, middle part).

The input situation for each test step is defined by combining classes of different classifications from the classification-tree. This is done by marking the appropriate tree elements in the main column of the combination table. This leads to a sequence of input situations. The duration of each input situation can be captured by the annotation of time tags in the rightmost column of the combination table (Fig. 3, middle part).

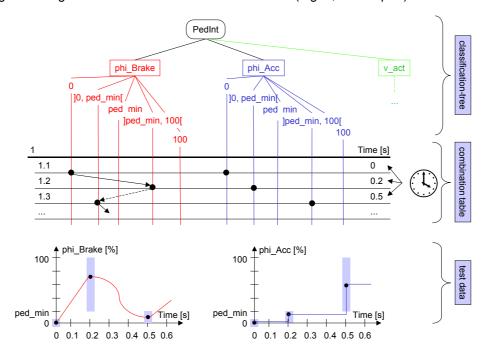


Figure 3: Defining test sequences

Time-dependent behavior, i.e. changing the values of a input over time in successive test steps can be modeled by marking different classes of the classification corresponding to that input. The stimulus signal in the respective test step is thus restricted to the part-interval or single value of the marked class. The combination of the marked input classes of a test step determines the input of the test object at the respective time (Fig. 3, middle part).

The intermediate values of the individual stimuli signals are defined by transitions between markings of consecutive test steps. Different transition types represent different signal shapes (e.g. ramp, step function, sine; cf. Tab 3). In this way stimuli signals can be described in an abstract manner with the help of parameterized, stepwise defined functions (Fig. 3, middle and lower part).

Table 3: Assignment of signal shapes for line types

signal shape	transition type		
step			
ramp			
sine / spline			

Further test sequences can be described underneath the classification-tree by using the procedure mentioned above.

Example PedInt

In a first test scenario (Fig 4, test scenario #1), the aim is to investigate the recognition of pedal activation with specific values, each of which are kept constant for a certain period of time.

A second test scenario (Fig 4, test scenario #2) checks the hysteresis properties of the test object. In order to do this, the pedal positions are varied, in the form of a ramp, over the entire value area, each going upwards and downwards once. In doing so, classes are selected arbitrarily for v_act.

In the case of different pedal positions, a third test scenario (Fig 4, test scenario #3), passes over the entire non-negative speed area linearly for each one. phi_Acc is varied over all the classes in 2s rhythm, whilst the other two classifications are each simulated with unchanging behavior (phi_Brake = 0, v_act increases linearly in a standard way).

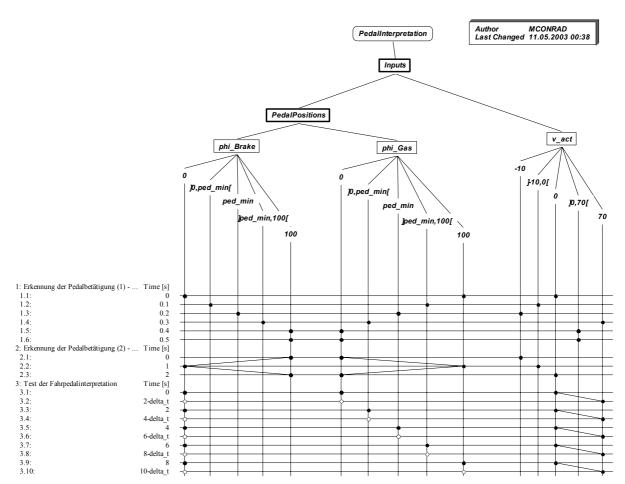


Figure 4: Classification-tree with test scenarios for PedInt

TEST DATA REFINEMENT

In a fourth step, the abstract test scenarios will be refined into signal waveforms.

The test scenarios gained so far contain abstracted stimulus information because only equivalent classes, but no specific data have been used. Thus, in a further step, the test data is instantiated by the use of specific numbers.

The test scenarios defined in the combination table form signal corridors for the individual input signals, within which the actual courses of input values must be located. The borders of the equivalent classes form the constraints of the value ranges at the respective test steps. This step is illustrated in the lower part of Fig. 3.

Example PedInt

On the basis of the uniformity hypothesis any value within the marked interval can be selected when determining the values at the base points. Within the scope of this example the principle of mean value testing is being used, i.e. in each case the mean values of the equivalence classes selected are used as test data (ped_min = 5 %, Fig. 5).

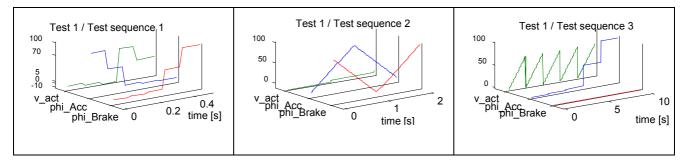


Figure 5: Test data curves PedInt

What has been presented so far, is how to systematically determine and compactly and adequately describe test scenarios for the black-box test of automotive control software with the help of CTM/ES.

THE UNDERLYING TEST MODEL

CTM/ES can be seen as a model-based testing approach [EW01], which is based on an abstract model of the input data space of the test object.

The data-oriented test input partitioning of the CTM/ES implicitly applies a parallel state-machine model to describe the test input space. Each variable of the effective test input interface forms one of the parallel parts (AND-states) of the state machine. The states denote the individual equivalence classes defined for the variable. Test sequences can be viewed as pathes through such a test model.

Making these underlying test models explicit, allows to compare the CTM/ES with other model-based testing approaches. Furthermore, the test model can be used to formalize different test coverage criteria.

Example PedInt

Fig. 6 shows the implicit test model for the PedInt example. Fig. 7 illustrates how test scenario #3 (cf. (Fig 4) can be understood as a path through the test model.

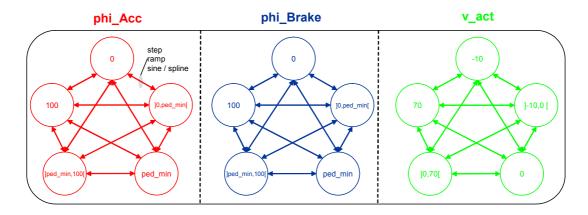


Figure 6: Underlying test model PedInt

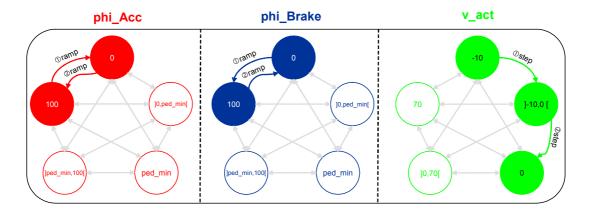


Figure 7: Visualization of test scenario #3

A TEST STRATEGY FOR AUTOMOTIVE CONTROL SOFTWARE

CTM/ES can be deployed as a black-box test design technique, independently from an actual development process. The only pre-requisites are the description of the test object's behaviour and interface.

A typical field of application is the deployment of CTE/ES within the framework of model-based development of automotive control software.

MODEL-BASED DEVELOPMENT

Model-based development can be regarded as an answer of numerous automobile OEMs and suppliers to the increased demands on the development of embedded software [KCF+04]. This innovative development approach is characterized by the integrated deployment of executable graphical models for specification, design and implementation, using commercial modeling and simulation environments such as MATLAB/Simulink/Stateflow [Matlab]. These tools use block diagrams and extended state machines as modeling notations.

Very early in this development procedure an executable model of the control software (functional model) is created, which can be simulated as well as tested. This executable model is used throughout the downstream development process and forms the 'blueprint' for the automatic or manual coding of the embedded software and its integration into the electronic control unit (ECU). The seamless utilization of models facilitates highly consistent and efficient development.

MODEL-BASED TEST STRATEGY

Within the model-based development procedure it is essential to subject the software and its preliminary stages (models) to an appropriate combination of testing techniques in order to detect errors and produce confidence in its correct functioning. Such *test strategies* have to include combinations of functional and structural testing techniques. Furthermore, they must take into account the specifics of model-based development and especially the existence of an executable model in an adequate way.

The systematic selection of test scenarios from the functional specification, the interfaces, as well as the executable model of the embedded software forms the focal point of such a model-based test strategy. In addition, an adequate structural test criterion is defined on model level, with the help of which the coverage of the tests thus determined can be evaluated and the definition of complementary tests can be controlled. If sufficient test coverage has thus been achieved on model level, the functional as well as the structural scenarios can be reused for testing the control software generated from the model and the control unit within the framework of back-to-back tests. In this way, the functional equivalence between executable model and derived forms of representation can be verified (see [CFS04, Con04]). The model-based test strategy, which results from the above mentioned steps, is schematically depicted in Fig. 11.

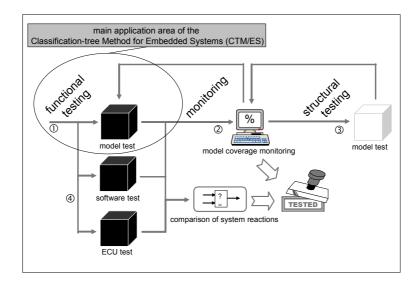


Figure 8: Model-based test strategy

TOOL-SUPPORT

The systematic definition of functional test scenarios with CTM/ES is supported by the *classification-tree editor CTE/ES* [Mis04, CTE/ES]. Comprehensive automation of functional testing on model level (step ①) is possible with the *model-based test environment MTest* [LBE+04, MTest], which utilizes the CTE/ES for the test design.

Monitoring of model coverage (step ②) can be carried out by the Simulink model coverage tool [SL-VV] or the monitoring component of Reactis Tester [Reactis].

An automation of structural test design on model level (step ③) is possible with Reactis Tester [Reactis, Ran03] or with an extended version of the evolutionary testing system ET [BCS+03, WSB01].

The execution of back-to-back tests between model and the control software derived from it (step ④) can again be carried out with MTest. In order to also check the embedding of the software in the control unit, test scenarios can be exported from MTest to AutomationDesk [AUD, LRR03]. There, it can be reused for the (Hardware-in-the-Loop) test of the control unit.

The results of back-to-back tests can be analyzed with the help of MEval [CFP03, MEval].

SUMMARY AND CONCLUSION

The selection of suitable, i.e. error-sensitive, test scenarios (test design) is the most crucial activity for a trust-worthy software test. It ultimately determines the scope and quality of the test. Moreover, an appropriate description of the test scenarios used is essential for the human tester, particularly when developing embedded automotive software.

A systematic approach for the selection of test scenarios and a notation for their appropriate description, must therefore form the core elements of a testing approach for automotive control software. In order to allow testing to begin at an early stage, test design should build upon development artifacts available early on, such as the specification or an executable model.

Current practice, however, is characterized by test scenario design according to a gut feel approach, leading to test gaps and test redundancies. In this paper a new, more systematic way of testing automotive control software has been presented. The core element of the approach is the Classification-tree Method for Embedded Systems (CTM/ES).

Based on the data-oriented partitioning of the input domain into equivalence classes, time-variable test scenarios can be derived systematically and described graphically. A compact, problem-oriented representation, suitable for a human tester and containing a high potential for reusability, is transformed into a technical representation which is suited to the test objects' stimulation.

CTM/ES can be seen as a model-based testing approach, which is based on an abstract model of the input data space of the test object. Making this underlying test model (a parallel state-machine) explicit, eases the comparison of CTM/ES with other model-based testing approaches and allows to formalize different test coverage criteria.

The CTM/ES enables the unified representation of test scenarios which result from other (functional and structural) test design techniques. It can thus be employed as a universal means of description for almost all the test scenarios which arise as part of automotive control software testing. Another distinct advantage of the CTM/ES is that it can even be applied in situations in which the specification is incomplete or out of date.

The CTM/ES has recently been successfully employed in different software development projects. One of the main application areas is the testing of automotive software developed in a model-based way. To seamlessly support this application area, functional (black-box) testing with the CTM/ES was combined with structural (white-box) testing on model level. This combination, together with the subsequent reuse of the test scenarios for software and ECU testing, leads up to an integrated model-based test strategy.

Embedded automotive software which has been developed in a model-based way can be tested systematically and efficiently by means of the CTM/ES and the tools based thereon, such as the classification-tree editor CTE/ES, as well as the model-based test environment MTest.

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His main competencies are within the areas of model-based development and quality assurance of embedded automotive software. He co-developed the model-based testing tools CTE/ES, MTest and MEval. He is member of the Special Interest Group for Testing, Analysis and Verification of Software in the German Computer Society (GI TAV) and the MathWorks Automotive Advisory Board (MAAB).

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